

### PROTECTION PRODUCTS - EMIClamp®

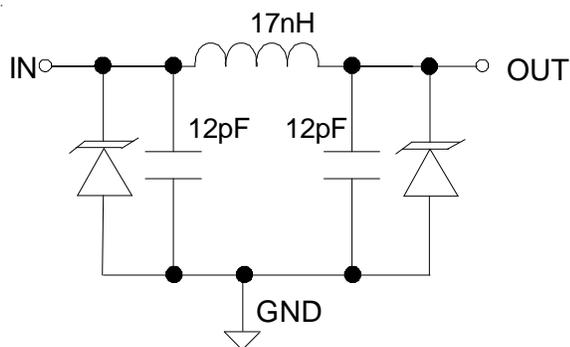
#### Description

The EClamp® 2506K device is a low pass filter array with integrated TVS diodes. It is designed to suppress unwanted EMI/RFI signals and provide electrostatic discharge (ESD) protection in portable electronic equipment. This state-of-the-art device utilizes solid-state silicon-avalanche technology for superior clamping performance and DC electrical characteristics. They have been optimized for **protection of color LCD panels** in cellular phones and other portable electronics.

The device consists of six identical circuits comprised of TVS diodes for ESD protection, and a 3-pole inductor - capacitor network for EMI/RFI filtering. A typical inductor value of 17nH and a capacitor value of 12pF are used to achieve 20dB minimum attenuation from 800MHz to 2.7GHz. The TVS diodes provide effective suppression of ESD voltages in excess of  $\pm 15\text{kV}$  (air discharge) and  $\pm 8\text{kV}$  (contact discharge) per IEC 61000-4-2, level 4.

The EClamp2506K is in a 12-pin, SLP2513P12 package. It measures 2.5 x 1.3 x 0.50mm. The leads are spaced at a pitch of 0.4mm and are finished with lead-free NiPdAu. The small package makes it ideal for use in portable electronics such as cell phones, digital still cameras, and PDAs.

#### Circuit Diagram (Each Line)



**Device Schematic (6X)**

#### Features

- ◆ Bidirectional EMI/RFI filter with integrated TVS for ESD protection
- ◆ ESD protection to **IEC 61000-4-2 (ESD) Level 4,  $\pm 15\text{kV}$  (air),  $\pm 8\text{kV}$  (contact)**
- ◆ Filter performance: 20dB minimum attenuation 800MHz to 2.7GHz
- ◆ TVS working voltage: 5V
- ◆ Inductor: 17nH (Typical)
- ◆ Capacitors: 12pF (Typical at VR = 2.5V)
- ◆ Protection and filtering for six lines
- ◆ Solid-state technology

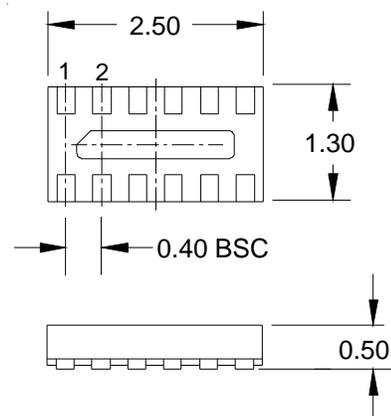
#### Mechanical Characteristics

- ◆ SLP2513P12 12-pin package
- ◆ Pb-Free, Halogen Free, RoHS/WEEE Compliant
- ◆ Nominal Dimensions: 2.5 x 1.3 x 0.50mm
- ◆ Lead Pitch: 0.4mm
- ◆ Lead finish: NiPdAu
- ◆ Marking : Marking Code and Date Code
- ◆ Packaging : Tape and Reel

#### Applications

- ◆ Color LCD Protection
- ◆ Cell Phone CCD Camera Lines
- ◆ Clamshell Cell Phones

#### Package Configuration



**12 Pin SLP package (Bottom Side View)  
Nominal Dimensions in mm**

**PROTECTION PRODUCTS**
**Maximum Ratings**

Rating	Symbol	Value	Units
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	$V_{ESD}$	+/- 20 +/- 15	kV
Junction Temperature	$T_J$	125	°C
Operating Temperature	$T_{op}$	-40 to +85	°C
Storage Temperature	$T_{STG}$	-55 to +150	°C

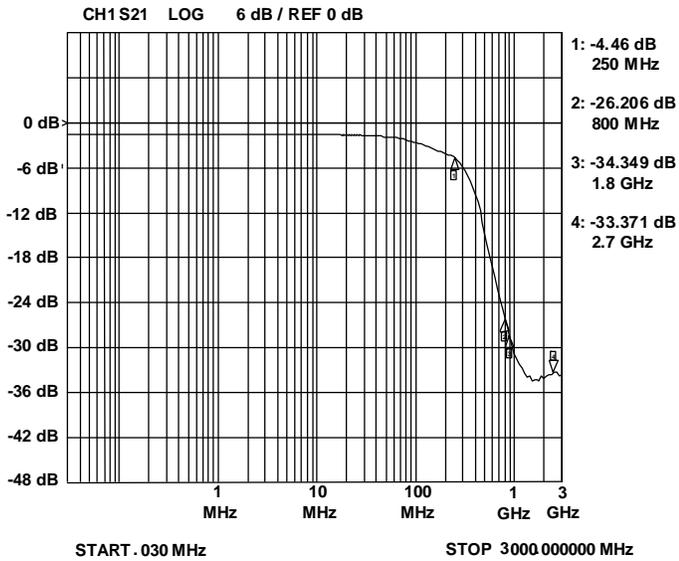
**Electrical Characteristics (T = 25°C)**

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
TVS Reverse Stand-Off Voltage	$V_{RWM}$				5	V
TVS Reverse Breakdown Voltage	$V_{BR}$	$I_t = 1mA$	6	8	10	V
TVS Reverse Leakage Current	$I_R$	$V_{RWM} = 3.3V$		0.03	0.1	μA
DC Resistance	$R_{DC}$			18		Ohms
Filter Cut-Off Frequency	fc	$Z_{Source} = Z_{Load} = 50 \text{ Ohms}$		250		MHz
Inductance	L			17		nH
Capacitance	$C_1, C_2$	$V_R = 2.5V, f = 1MHz$	10	12	15	pF
Total Capacitance	$C_1 + C_2$	Input to Gnd, Each Line $V_R = 2.5V, f = 1MHz$	20	24	30	pF

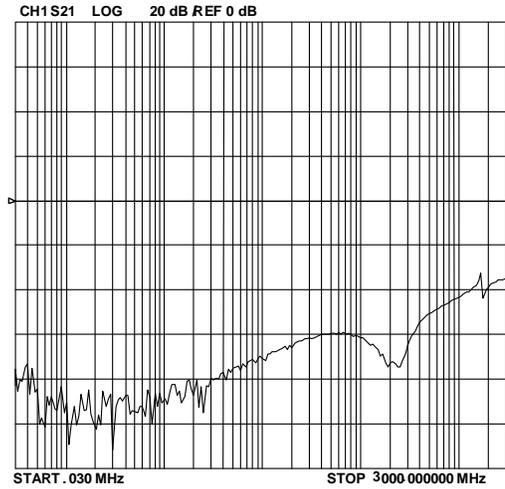
## PROTECTION PRODUCTS

### Typical Characteristics

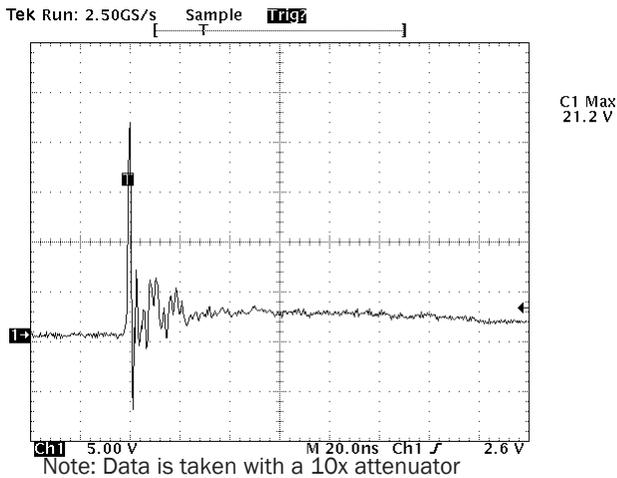
**Typical Insertion Loss S21 (Each Line)**



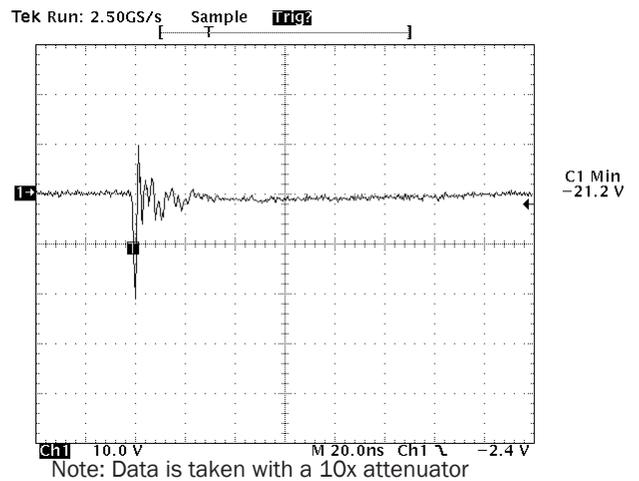
**Analog Crosstalk (Each Line)**



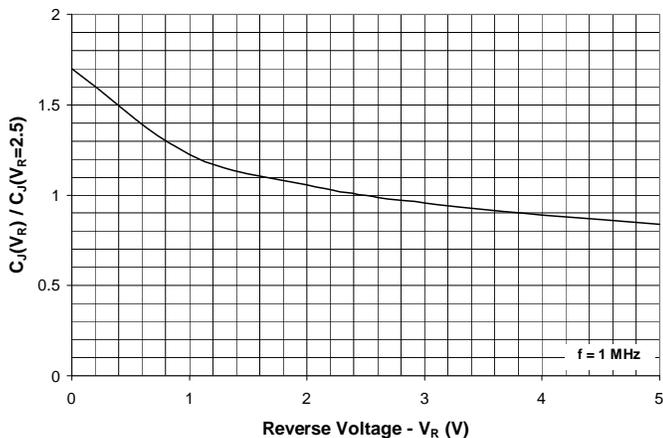
**ESD Clamping (+8kV Contact)**



**ESD Clamping (-8kV Contact)**



**Capacitance vs. Reverse Voltage (Normalized to 2.5 volts)**

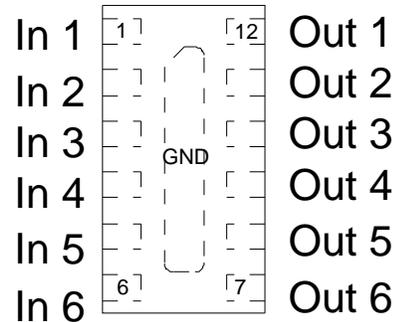


## PROTECTION PRODUCTS

### Device Connection

The EClamp2506K is comprised of four identical circuits each consisting of a low pass filter for EMI/RFI suppression and dual TVS diodes for ESD protection. The device is in a 12-pin SLP package. Electrical connection is made to the 12 pins located at the bottom of the device. A center tab serves as the ground connection. The device has a flow through design for easy layout. Pin connections are noted in Figure 1. All path lengths should be kept as short as possible to minimize the effects of parasitic inductance in the board traces. Recommendations for the ground connection are given below.

**Figure 1 - Pin Identification and Configuration (Top Side View)**



### Ground Connection Recommendation

Parasitic inductance present in the board layout will affect the filtering performance of the device. As frequency increases, the effect of the inductance becomes more dominant. This effect is given by Equation 1.

**Equation 1: The Impedance of an Inductor at Frequency XLF**

$$X_{LF}(L, f) = 2 * \pi * f * L$$

Where:

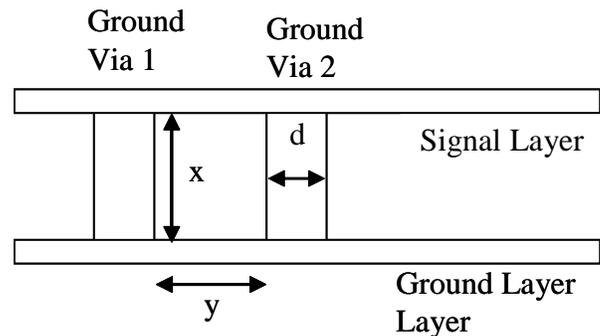
L = Inductance (H)

f = Frequency (Hz)

Via connections to the ground plane form rectangular wire loops or ground loop inductance as shown in Figure 2. Ground loop inductance can be reduced by using multiple vias to make the connection to the ground plane. Bringing the ground plane closer to the signal layer (preferably the next layer) also reduces ground loop inductance. Multiple vias in the device ground pad will result in a lower inductive ground loop over two exterior vias. Vias with a diameter  $d$  are separated by a distance  $y$  run between layers separated by a distance  $x$ . The inductance of the loop path is given by Equation 2. Thus, decreasing distance  $x$  and  $y$  will reduce the loop inductance and result in better high frequency filter characteristics.

Pin	Identification
1 - 6	Input Lines
7 - 12	Output Lines
Center Tab	Ground

**Figure 2 - Inductance of Rectangular Wire Loops**



**Equation 2: Inductance of Rectangular Wire Loop**

Where:

$d$  = Diameter of the wire (in)

$x$  = Length of wire loop (in)

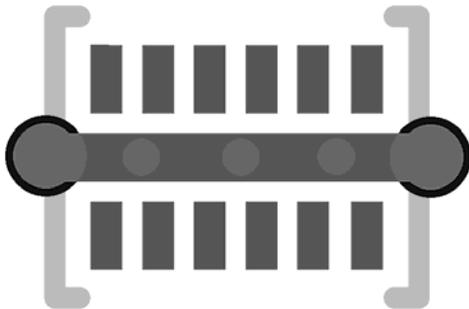
$y$  = Breadth of wire loop (in)

**PROTECTION PRODUCTS**

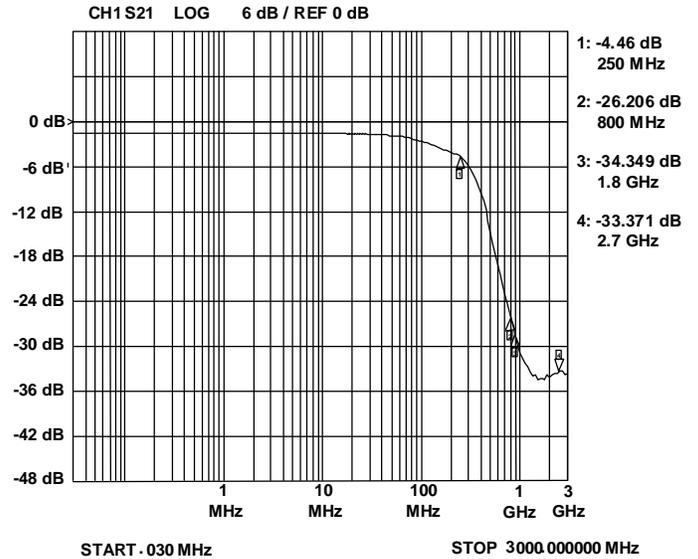
**Applications Information**

Figure 3 shows the recommended device layout. The ground pad vias have a diameter of 0.008 inches (0.20 mm) while the two external vias have a diameter of 0.010 inches (0.250mm). The internal vias are spaced approximately evenly from the center of the pad. The designer may choose to use more vias with a smaller diameter (such as 0.005 inches or 0.125mm) since changing the diameter of the via will result in little change in inductance (i.e. the log function in Equation 2 is highly insensitive to parameter d) . Figure 4 shows a typical insertion loss (S21) plot for the device using Semtech’s filter evaluation board with 50 Ohm traces and the recommended via configuration.

**Figure 3 - Recommended Layout Using Ground Vias**

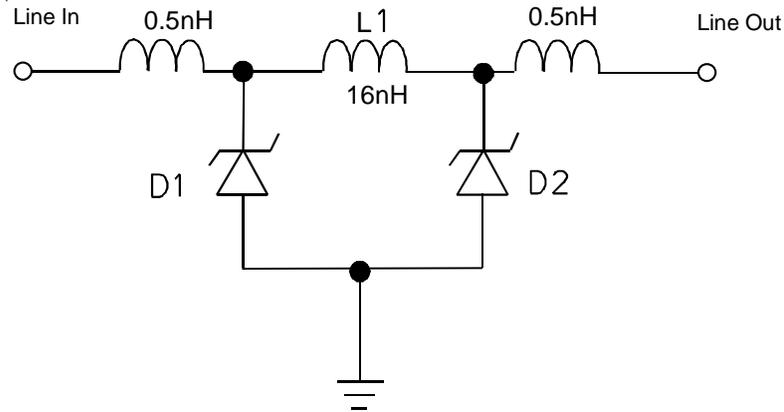


**Figure 4 - Filter Characteristics Using Recommended Layout with Internal Vias**



## PROTECTION PRODUCTS

### Applications Information - Spice Model

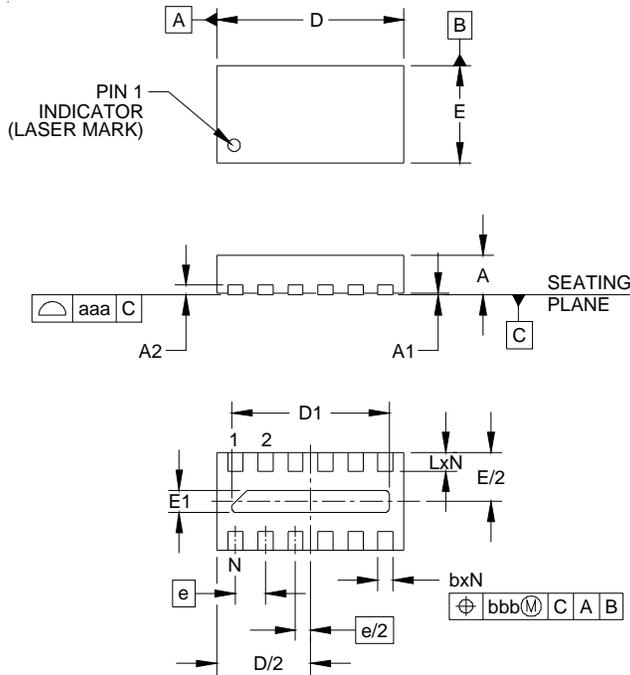


**EClamp2506K Spice Model**

EClamp2506K Spice Parameters			
Parameter	Unit	D1 (TVS)	D2 (TVS)
IS	Amp	1.48E-14	1.48E-14
BV	Volt	7.44	7.44
VJ	Volt	0.75	0.75
RS	Ohm	0.53	0.53
IBV	Amp	1E-3	1E-3
CJO	Farad	19.4E-12	19.4E-12
TT	sec	2.541E-9	2.541E-9
M	--	0.25	0.25
N	--	1.1	1.1
EG	eV	1.11	1.11

## PROTECTION PRODUCTS

### Outline Drawing - SLP2513P12

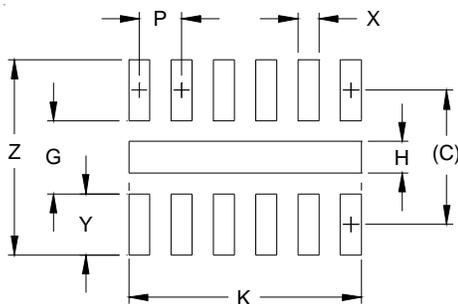


DIM	INCHES			MILLIMETERS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	.018	.020	.022	0.45	0.50	0.55
A1	.000	.001	.002	0.00	0.02	0.05
A2	(.005)			(0.13)		
b	.006	.008	.010	0.15	0.20	0.25
D	.096	.098	.101	2.45	2.50	2.575
D1	.079	.083	.087	2.00	2.10	2.20
E	.049	.051	.054	1.25	1.30	1.375
E1	.008	.012	.016	0.20	0.30	0.40
e	.016 BSC			0.40 BSC		
L	.008	.010	.012	0.20	0.25	0.30
N	12			12		
aaa	.003			0.08		
bbb	.004			0.10		

**NOTES:**

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

### Land Pattern - SLP2513P12



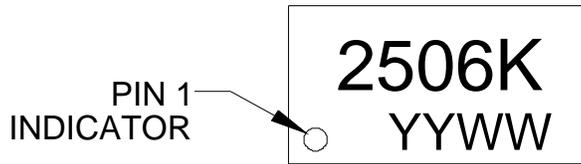
DIM	DIMENSIONS	
	INCHES	MILLIMETERS
C	(.050)	(1.27)
G	.027	0.69
H	.012	0.30
K	.087	2.20
P	.016	0.40
X	.008	0.20
Y	.023	0.58
Z	.073	1.85

**NOTES:**

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

## PROTECTION PRODUCTS

### Marking



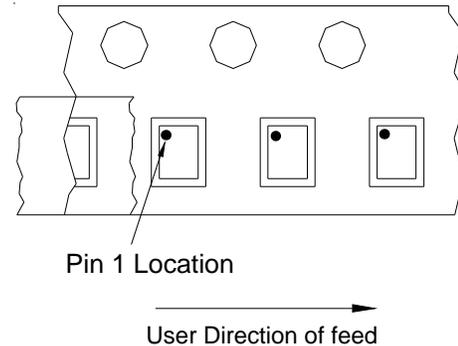
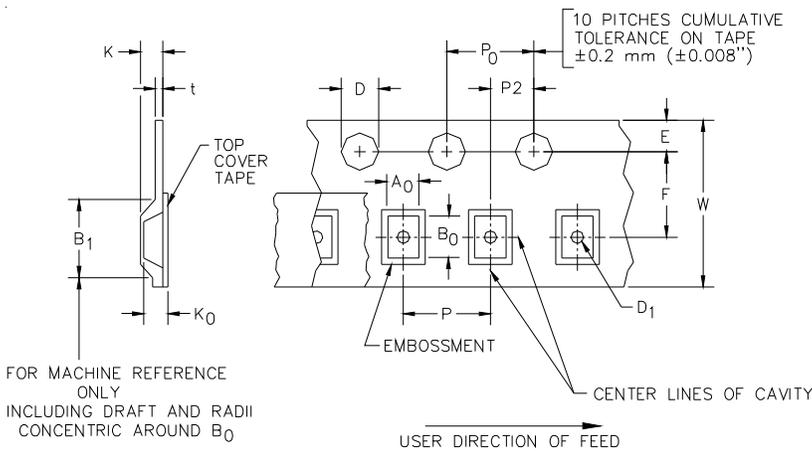
### Ordering Information

Part Number	Qty per Reel	Reel Size
EClamp2506K.TCT	3000	7 Inch

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Note: YYWW = Date Code

### Tape and Reel Specification



### Device Orientation in Tape

A0	B0	K0
1.51 +/-0.10 mm	2.71 +/-0.10 mm	0.66 +/-0.10 mm

Tape Width	B, (Max)	D	D1	E	F	K (MAX)	P	P0	P2	T(MAX)	W
8 mm	4.2 mm (.165)	1.5 + 0.1 mm - 0.0 mm (0.59 +.005 - .000)	0.8 mm ±0.05 (.031)	1.750±.10 mm (.069±.004)	3.5±0.05 mm (.138±.002)	2.4 mm (.094)	4.0±0.1 mm (.157±.004)	4.0±0.1 mm (.157±.004)	2.0±0.05mm (.079±.002)	0.4 mm (.016)	8.0 mm + 0.3 mm - 0.1 mm (.312±.012)

### Contact Information

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