



SPECIFICATION

• Supplier : Samsung electro-mechanics • Samsung P/N : CL31C100JHFNNNF

• Product : Multi-layer Ceramic Capacitor • Description : CAP, 10pF, 630V, ±5%, C0G, 1206

A. Samsung Part Number

<u>CL</u> <u>31</u> <u>C</u> <u>100</u> <u>J</u> <u>H</u> <u>F</u> <u>N</u> <u>N</u> <u>N</u> <u>F</u> ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

1	Series	Samsung Multi-layer Ceramic Capacitor		
2	Size	1206 (inch code)	L: 3.2 ± 0.15 mm	W: 1.6 ± 0.15 mm
	Distanti	000		NI:
(3)	Dielectric	C0G	8 Inner electrode	Ni
4	Capacitance	10 pF	Termination	Cu
⑤	Capacitance	±5 %	Plating	Sn 100% (Pb Free)
	tolerance		Product	Normal
6	Rated Voltage	630 V	Special	Reserved for future use
7	Thickness	1.25 ± 0.15 mm	① Packaging	Embossed Type,13"reel(10,000ea)

B. Samsung Reliability Test and Judgement condition

	Performance	Test condition	
Capacitance	Within specified tolerance	1Mb±10% 0.5~5Vrms	
Q	600 min		
Insulation	More than 500Mohm⋅μF	500±50 Vdc 60~120 sec.	
Resistance			
Appearance	No abnormal exterior appearance	Visual inspection	
Withstanding	No dielectric breakdown or	150% of the rated voltage	
Voltage	mechanical breakdown		
Temperature	COG		
Characteristics	(From -55 ℃ to 125 ℃, Capacitance change should be within ±30PPM/℃)		
Adhesive Strength	No peeling shall be occur on the	500g·F, for 10±1 sec.	
of Termination	terminal electrode		
Bending Strength	Capacitance change: within ±5%	Bending to the limit (1mm)	
		with 1.0mm/sec.	
Solderability	More than 75% of terminal surface	SnAg3.0Cu0.5 solder	
	is to be soldered newly	245±5℃, 3±0.3sec.	
		(preheating : 80~120 ℃ for 10~30sec.)	
Resistance to	Capacitance change: within ±2.5%	Solder pot : 270±5℃, 10±1sec.	
Soldering heat	Tan δ, IR : initial spec.		

	Performance	Test condition
Vibration Test	Capacitance change: within ±2.5%	Amplitude : 1.5mm
	Tan δ, IR : initial spec.	From 10Hz to 55Hz (return : 1min.)
		2hours × 3 direction (x, y, z)
Moisture	Capacitance change: within ±7.5%	With rated voltage
Resistance	Q: 133.33 min	40±2℃, 90~95%RH, 500 +12/-0 hour
	IR : More than 25‰· <i>μ</i> F	
High Temperature	Capacitance change: within ±3%	With 120% of the rated voltage
Resistance	Q: 300 min	Max. operating temperature
	IR : More than 50MΩ· <i>μ</i> F	1000+48/-0 hour
Temperature	Capacitance change : within ±2.5%	1 cycle condition
Cycling	Tan δ, IR : initial spec.	Min. operating temperature → 25 °C
		→ Max. operating temperature → 25°C
		5 cycles test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^{\circ}\!\text{C}$, 10sec. Max)

^{*} For the more detail Specification, Please refer to the Samsung MLCC catalogue.