

Multilayer Varistor for ESD pulse [DC voltage lines]

Series: **EZJS**









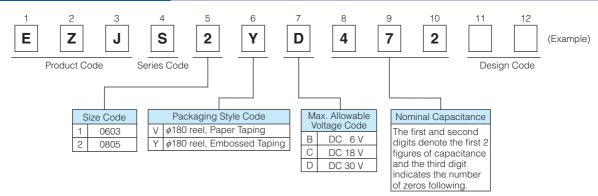
Features

- Excellent ESD suppression due to original advanced material technology
- Having large electrostatic resistance meeting IEC61000-4-2, Special Level 30 kV standard
- Having no polarity (bipolar) facilitated replacing Zener Diodes. Capable of replacing 2 Zener Diodes and 1 Capacitor.
- Lead-free terminal electrodes enabling great solderability
- RoHS compliant

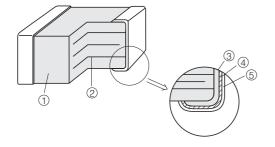
■ As for Packaging Methods, Handling Precautions

Please see Data Files

Explanation of Part Numbers

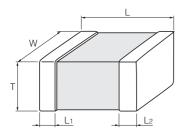


Construction



No.	Name					
1	Semiconductive Ceramics					
2	Internal electrode					
3		Substrate electrode				
4	Terminal electrode	Intermediate electrode				
(5)		External electrode				

Dimensions in mm (not to scale)



Size Code	Size(inch)	L	W	Т	L1, L2
1	0603	1.60±0.15	0.8±0.1	0.8±0.1	0.3±0.2
	0805	2.0±0.2	0±0.2 1.25±0.20 0.8±0.2 0	0.50±0.25	
2	0000	2.0±0.2	1.25±0.20	1.25±0.20	0.50±0.25

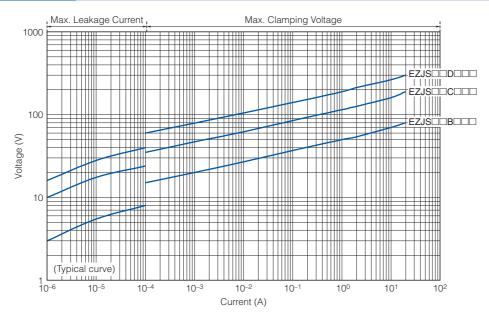


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Ratings an	a C	nara	cter	ISI	ICS

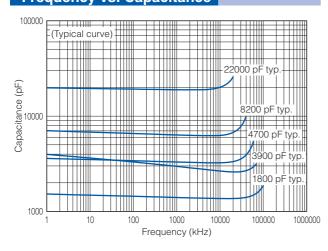
Size	Part No.	Maximum Allowable Voltage DC (V)	Nominal Varistor Voltage at 0.1 mA (V)	Capacitance at 1 kHz (pF)	Maximum ESD IEC61000-4-2
	EZJS1VB822	6	12	8200 typ.	
0603	EZJS1VC392	18	30	3900 typ.	
	EZJS1VD182	30	50	1800 typ.	Contact discharge:
	EZJS2VB223	6	12	22000 typ.	30 kV
0805	EZJS2YC822	18	30	8200 typ.	
	EZJS2YD472	30	50	4700 typ.	

- Operating Temperature Range: -40 to 85 °C
- * Avoid flow soldering

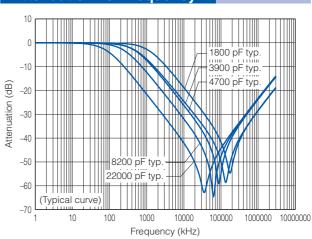
Voltage vs. Current



Frequency vs. Capacitance



Attenuation vs. Frequency





Varistor Characteristics and Equivalent Circuit

A Multilayer Varistor does not have an electrical polarity like zener diodes and is equivalent to total 3 pcs. of 2 zener diodes and 1 capacitor.

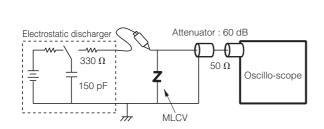
[Equivalent Circuit]

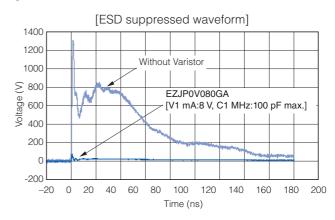


ESD Suppressive Effects

Typical effects of ESD suppression

Test conditions: IEC61000-4-2* Level 4 Contact discharge, 8 kV



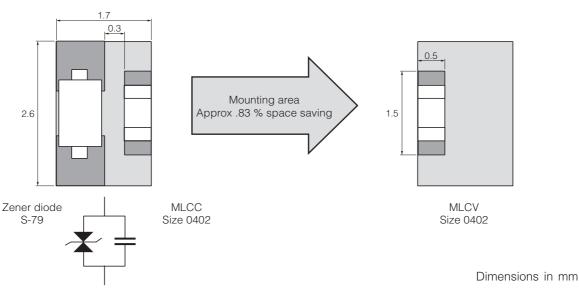


* IEC61000-4-2 ··· International Standard of the ESD testing method (HBM) for electronic equipment ability to withstand ESD generated from a human body. It sets 4 levels of severity

Severity	Level 1	Level 2	Level 3	Level 4
Contact discharge	2 kV	4 kV	6 kV	8 kV
Air discharge	2 kV	4 kV	8 kV	15 kV

Replacement of Zener diode

Replacing "Zener diode and Capacitor" with Multilayer Varistor saves both the mounting area and number of components used.



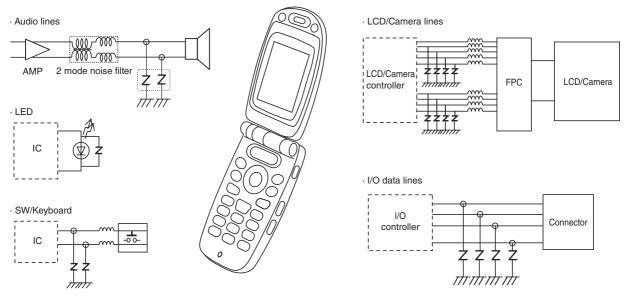


Recommended Applications

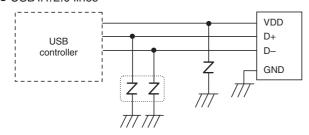
Applications		Series	Circuit DC 1k 1M 1G (Hz)		
Mobile phones, DSC, PC, PDA, HDD TV (PDP, LC etc.), DVD, DVC,	Series EZJZ, P Low capacitance (Cap. : 3 pF or less) Low capacitance (Cap. : 20 to 680 pF)			DC to GHz Antenna, RF circuit, LVDS USB, IEEE1394, HDMI etc.	
Game consoles, Audio equipment					DC to tens of Hz PWR, SW, Audio terminals LCD, RS232C, etc.
PWR, Photoelectronic sensors, SSR, Motors, Pressure sensors, Proximity switches	Series EZJS	High capacitance (Cap.: 1800 to 22000 pF)			DC to several kHz PWR, SW, Audio terminals etc.

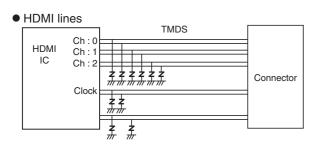
Applications

Mobile Phone

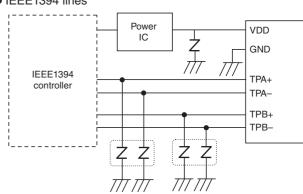


● USB1.1/2.0 lines





• IEEE1394 lines





Performa	ance and '	Testing	Methods
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Characteristics	Specifications	Testing Method		
Standard test conditions		Electrical characteristics shall be measured under the following conditions. Temp. : 5 to 35 °C, Relative humidity : 85 % or less		
Varistor voltage	To meet the specified value.	The Varistor voltage is the voltage (V_c ,or V_{cmA}) between both end terminals of a Varistor when specified current (CmA) is applied to it. The measurement shall be made as quickly as possible to avoid heating effects.		
Maximum allowable voltage	To meet the specified value.	The maximum DC voltage that can be applied continuously to a varistor		
Capacitance	To meet the specified value.	Capacitance shall be measured at the specified frequency, bias voltage 0 V, and measuring voltage 0.2 to 2 Vrms.		
Maximum peak current	To meet the specified value.	The maximum current measured (Varistor voltage tolerance is within ±10 %) when a standard impulse current of 8/20 µ seconds is applied twice with an interval of 5 minutes.		
Maximum ESD	To meet the specified value.	The maximum ESD measured (while the varistor voltage is within ±30 % of its nominal value) when exposed to ESD 10 times (five times for each positive-negative polarity) based on IEC61000-4-2.		
Solder ability	To meet the specified value.	The part shall be immersed into a soldering bath under the conditions below. Solder: H63A Soldering flux : Ethanol solution of rosin (Concentration approx. 25 wt%) Soldering temp. : 230±5 °C Period : 4±1 s Soldering position: Immerse both terminal electrodes until they are completely into the soldering bath.		
Resistance to soldering heat	ΔVc / Vc : within ±10 %	After the immersion, leave the part for 24 ±2 hours under the standard condition, then evaluate its characteristics. Soldering conditions are specified below: Soldering conditions: 270 °C, 3 s / 260 °C, 10 s Soldering position: Immerse both terminal electrodes until they are completely into the soldering bath.		
Temperature cycling	ΔVc / Vc : within ±10 %	After repeating the cycles stated below for specified number of times, leave the part for 24±2 hours, then evaluate its characteristics. Cycle: 5 cycles Step Temperature Period 1 Max. Operating Temp. 30±3 min 2 Ordinary temp. 3 min max. 3 Min. Operating Temp. 30±3 min 4 Ordinary temp. 3 min max.		
Biased Humidity	ΔVc / Vc : within ±10 %	After conducting the test under the conditions specified below, leave the part 24±2 hours, then evaluate its characteristics. Temp. : 40±2 °C Humidity : 90 to 95 %RH Applied voltage : Maximum allowable voltage (Individually specified) Period : 500+24 / 0 h		
High temperature exposure (dry heat)	ΔVc / Vc : within ±10 %	After conducting the test under the conditions specified below, leave the part 24 ±2 hours, then evaluate its characteristics. Temp. : Maximum operating temperature ±3 °C (Individually specified) Applied voltage : Maximum allowable voltage (Individually specified) Period : 500+24 / 0h		