2SC1846

Silicon NPN epitaxial planar type

For medium output power amplification Complementary to 2SA0885

■ Features

- ullet Low collector-emitter saturation voltage $V_{CE(sat)}$
- Output of 3 W can be obtained by a complementary pair with 2SA0885
- TO-126B package which requires no insulation plate for installation to the heat sink

■ Absolute Maximum Ratings T

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	45	V
Collector-emitter voltage (Base open)	V _{CEO}	35	V
Emitter-base voltage (Collector open)	$V_{\rm EBO}$	5	V
Collector current	I_{C}	1	A
Peak collector current	I _{CP}	1.5	A
Collector power dissipation	P _C	1.2	w
		5.0 *	40/10
Junction temperature	T_{j}	150	o °C
Storage temperature	T _{stg}	-55 to +150	°C
			-17

Unit: mm $8.0^{+0.5}_{-0.1}$ 3.2±0.2

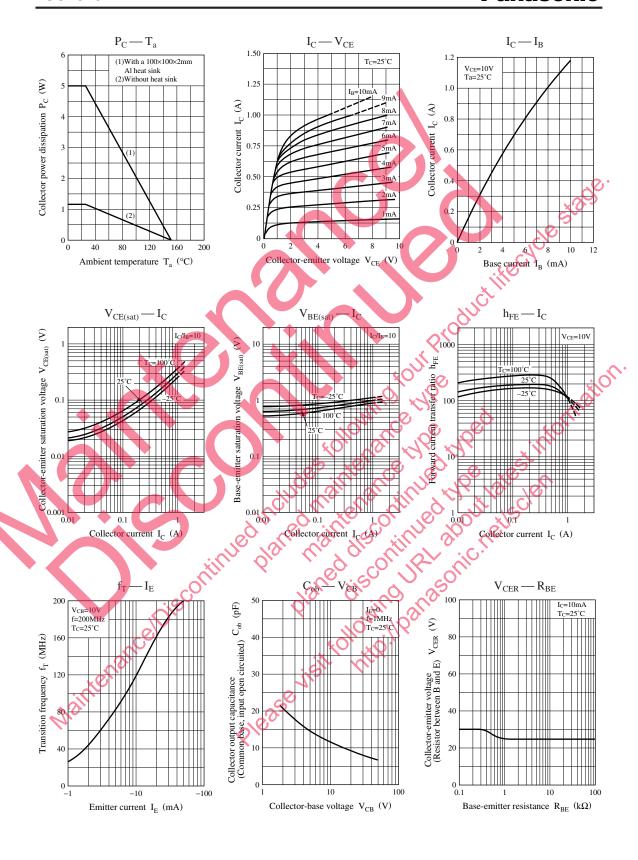
Ellitter-base voltage (Collector open)	EBO	3		(O)		T	O-126B-A	1 D1
Collector current	I_{C}	1	A	10,00	<u> </u>	1	U-126B-A	1 Package
Peak collector current	I _{CP}	1.5	A	3 141		_		do
Collector power dissipation	P _C	1.2	WON	CO _0,	20		٧)	
		5.0 *	40/10 3/	, 76	×16.		illi	
Junction temperature	T _j	150	aintenan	.e. ~ c) O.	~	3	
Storage temperature	T _{stg}	-55 to +150	300	المراد	100	Xe	20	
Note) *: With a $100 \times 100 \times 2$ mm Al h	eat sink	-Clo	10 10,	dille	ω'^*	10.		
11 + 60		16, 9	i de la	(1, 60	00	· x/c		
■ Electrical Characteristics T	$\Gamma_a = 25$ C	± 300	ail is	rinued himber	the the	S		
Parameter	Symbol		Conditions	10/	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = 1 \text{ mA}$	$\mathbf{I}_{\mathbf{E}} = 0$, V' c	O 45			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 2 \text{ mA}$	$I_{\rm B} = 0$	(D) NO.	35			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 20$	$V, I_E = 0$	120			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 20$	$V, I_B = 0$	116			100	μΑ
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = 5 \text{ V}$	$I_{\rm C} \stackrel{>}{=} 0$	•			10	μΑ
Forward current transfer ratio	h _{FE1} *	V _{CE} = 10	$V, I_C = 500 \text{ mA}$		85		340	_
in the	h _{FE2}	$V_{CE} = 5 \text{ V}$	$I_{\rm C} = 1 \text{ A}$		50			
Collector-emitter saturation voltage	V _{CE(sat)}	Jc≥ 500 n	$nA, I_B = 50 mA$				0.5	V
Transition frequency	f	$V_{CB} = 10$	$V, I_E = -50 \text{ mA, f}$	= 200 MHz		200		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10$	$V, I_E = 0, f = 1 M$	Hz			20	pF
(Common base, input open circuited)								

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. *: Rank classification

Rank	Q	R	S
h_{FE1}	85 to 170	120 to 240	170 to 340

Publication date: February 2003 SJD00094BED



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