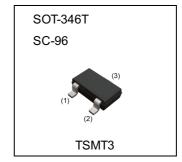


NPN 3.0A 50V Middle Power Transistor

Parameter	Value		
V _{CEO}	50V		
IC	3A		

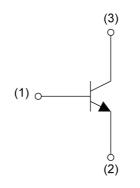
Outline



Features

- 1)Suitable for Middle Power Transistor
- 2) Complementary PNP Types:2SAR543R
- 3) Low saturation voltage, typically $V_{CE(sat)}$ =350mV(Max.) $(I_C/I_B$ =2A/100mA)

•Inner circuit



- (1) Base
- (2) Emitter
- (3) Collector

Application

LOW FREQUENCY AMPLIFIER, HIGH SPEED SWITCHING

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
2SCR543R	SOT-346T (TSMT3)	2928	TL	180	8	3000	NR

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	V_{CEO}	50	V
Emitter-base voltage	V _{EBO}	6	V
Callegtor gurrent	I _C	3	Α
Collector current	I _{CP} *1	6	Α
Davis a discinction	P _D *2	0.5	W
Power dissipation	P _D *3	1.0	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Offic	
Collector-base breakdown voltage	BV _{CBO}	I _C = 100μA	50	-	-	V	
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	50	-	-	V	
Emitter-base breakdown voltage	BV_{EBO}	I _E = 100μA	6	-	-	V	
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	1.0	μA	
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	-	-	1.0	μA	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 2A, I _B = 100mA	-	130	350	mV	
DC current gain	h _{FE}	V _{CE} = 3V, I _C = 100mA	180	-	450	-	
Transition frequency	f _T	$V_{CE} = 10V, I_{E} = -500 \text{mA},$ f = 100MHz	-	300	-	MHz	
Output capacitance	C _{ob}	$V_{CB} = 10V$, $I_E = 0A$, $f = 1MHz$	-	20	-	pF	
Turn-On time	t _{on}	I _C = 2A, I _{B1} = 200mA,	-	50	1	ns	
Storage time	t _{stg}	$I_{B2} = -200 \text{mA},$ $V_{CC} \approx 10 \text{V},$	-	450	-	ns	
Fall time	t _f	$R_L = 4.7\Omega$ See test circuit	-	85	-	ns	

^{*1} P_W=10ms, Single pulse

^{*2} Each terminal mounted on a reference land.

^{*3} Mounted on a 40×40×0.7mm ceramic board.

● Electrical characteristic curves(T_a = 25°C)

Fig.1 Ground Emitter Propagation Characteristics

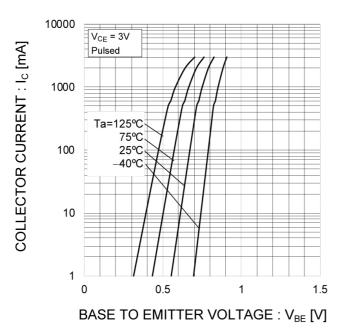
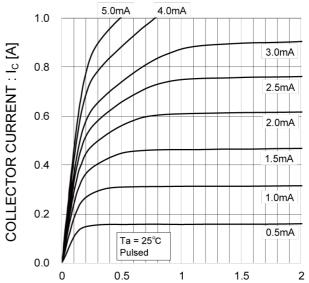


Fig.2 Typical Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]

Fig.3 DC Current Gain vs. Collector Current (I)

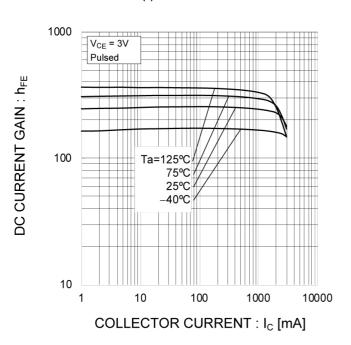
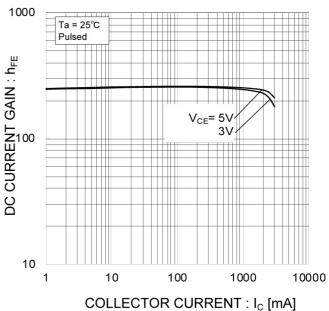


Fig.4 DC Current Gain vs. Collector Current (II)



● Electrical characteristic curves(T_a = 25°C)

Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current (I)

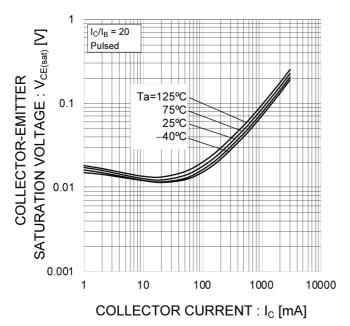


Fig.6 Collector-Emitter Saturation Voltage vs. Collector Current (II)

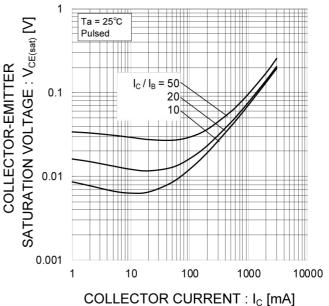


Fig.7 Base-Emitter Saturation Voltage vs. Collector Current

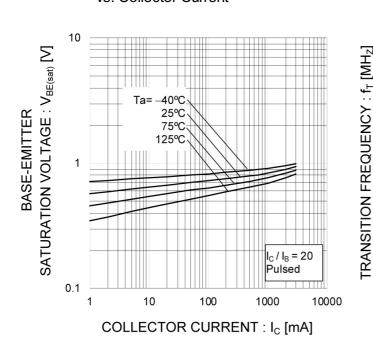
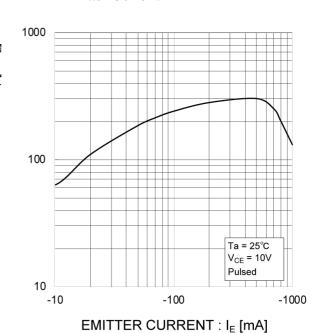


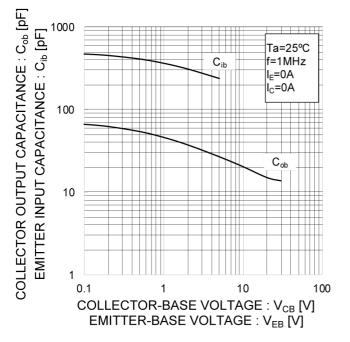
Fig.8 Gain Bandwidth Product vs. Emitter Current

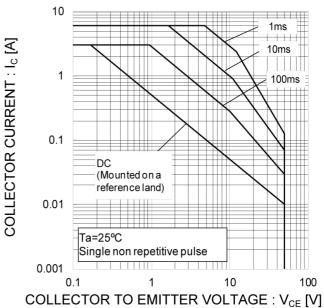


● Electrical characteristic curves(T_a = 25°C)

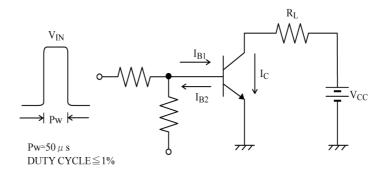
Fig.9 Emitter Input Capacitance vs.
Emitter-Base Voltage
Collector Output Capacitance vs.
Collector-Base Voltage

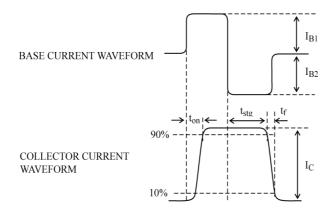
Fig.10 Safe Operating Area





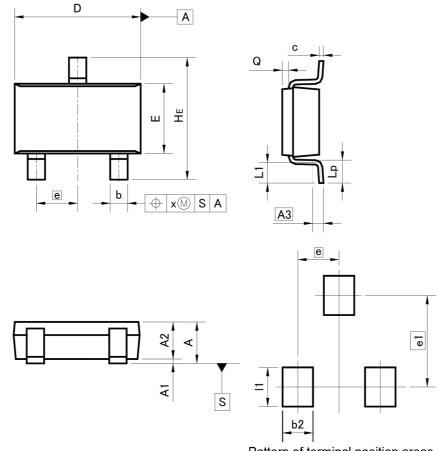
SWITCHING TIME TEST CIRCUIT





Dimensions

TSMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	I	1.00	-	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75 0.95 0.030		0.030	0.037
A3	0.:	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.9	0.95		37
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
х	_	0.20	_	0.008

DIM	MILIMETERS		INCHES		
	DIM	MIN	MAX	MIN	MAX
	b2		0.70	-	0.028
	e1	2.	10	0.0	83
	11	ı	0.90	ı	0.035

Dimension in mm/inches



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