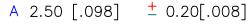
CPI1008K1R8R-10

UNCONTROLLED DOCUMENT

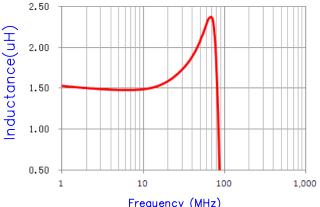
PHYSICAL DIMENSIONS:



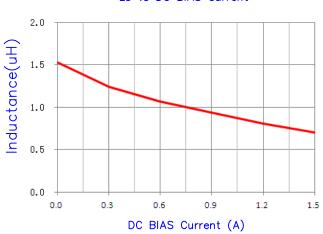
- B 2.00 [.079] ± 0.20[.008]
- $0.90 [.035] \pm 0.10[.004]$
- D 0.60 [.024] ± 0.20[.008]

A B

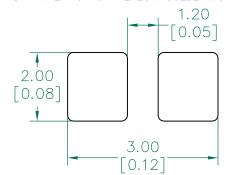




Ls vs DC BIAS Current







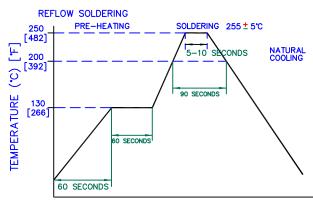
(For wave soldering. add 0.763 [0.030] to this dimension)

	ELECTRICAL CHARACTERISTICS:										
	L (μΗ) @ 1MHz ± 20%	DCR ($\frac{\Omega}{2}$) ± 25%	l (Max)								
Nom	1.80	0.1300									
Min	1.44	0.0975									
Max	2.16	0.1625	1500mA								

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 3000 PCS/REEL, EMBOSSED PLASTIC TAPE.
- 2. TERMINATION FINISH IS 100% MATTE Sn OVER Ni.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. I (MAX.) IS BASED ON THE MAXIMUM SUSTAINED CURRENT APPLIED WHILE MAINTAINING A MAXIMUM TEMPERATURE RISE OF 40°C OVER AMBIENT.
- 5. OPERATION TEMPERATURE TEMP: -55°C~+125°C (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS





_											
		DIMENSIONS ARE IN mm [INCHI	This print is the property of Lair	rd							
					Tech, and is loaned in confidence subject to return upon request (_	=	.I.	
F					with the understanding that no			2	ir(
					copies shall be made without the written consent of Laird Tech. A			u		I	
					rights to design or invention are		-				
Г					reserved.						
H			1		PROJECT/PART NUMBER:	F	REV	PART T	PE:	DRAWN BY:	
⊢	$\overline{}$				CPI1008K1R8R-10		C co-		CIDE	QU	
	С	UPDATE PAD DIMENSION ROHS	07/08/14	QU					FIRE	🕶	
Γ	В	UPDATE LAIRD LOGO AND NOTES 5	08/05/13	QU	DATE: 03/01/11	SCAL	E: N	TS	SHEET:		
F	Α	ORIGINAL DRAFT	03/01/11	QU	, ,	TOOL		13			
	REV	DESCRIPTION	DATE	INT	CPI1008K1R8R-10-C	IOOL	7	_	1	of 1	