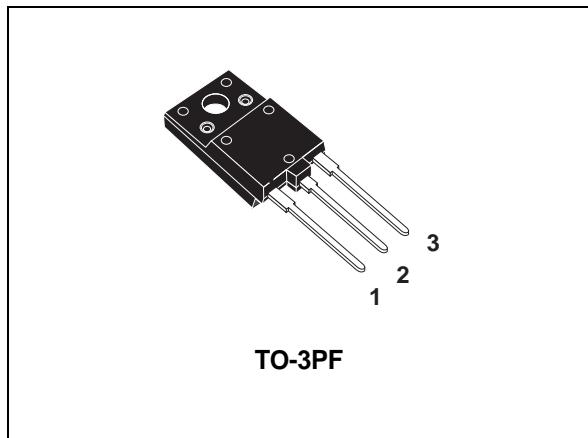
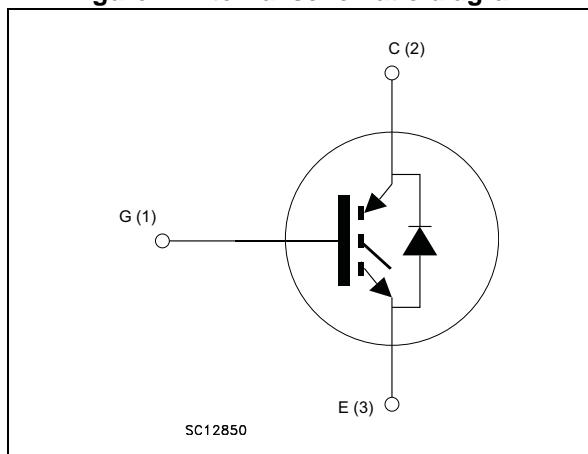


## Trench gate field-stop IGBT, V series 600 V, 30 A very high speed

Datasheet - production data



**Figure 1. Internal schematic diagram**



## Features

- Maximum junction temperature:  $T_J = 175 \text{ }^{\circ}\text{C}$
- Tail-less switching off
- $V_{CE(\text{sat})} = 1.85 \text{ V (typ.)} @ I_C = 30 \text{ A}$
- Tight parameters distribution
- Safe paralleling
- Low thermal resistance
- Very fast soft recovery antiparallel diode

## Applications

- Photovoltaic inverters
- Uninterruptible power supply
- Welding
- Power factor correction
- Very high frequency converters

## Description

This device is an IGBT developed using an advanced proprietary trench gate field stop structure. The device is part of the V series of IGBTs, which represent an optimum compromise between conduction and switching losses to maximize the efficiency of very high frequency converters. Furthermore, a positive  $V_{CE(\text{sat})}$  temperature coefficient and very tight parameter distribution result in safer paralleling operation.

**Table 1. Device summary**

Order code	Marking	Package	Packaging
STGFW30V60DF	GFW30V60DF	TO-3PF	Tube

## Contents

<b>1</b>	<b>Electrical ratings</b>	<b>3</b>
<b>2</b>	<b>Electrical characteristics</b>	<b>4</b>
2.1	Electrical characteristics (curves)	6
<b>3</b>	<b>Test circuits</b>	<b>11</b>
<b>4</b>	<b>Package mechanical data</b>	<b>12</b>
<b>5</b>	<b>Revision history</b>	<b>14</b>

# 1 Electrical ratings

**Table 2. Absolute maximum ratings**

Symbol	Parameter	Value	Unit
$V_{CES}$	Collector-emitter voltage ( $V_{GE} = 0$ )	600	V
$I_C$	Continuous collector current at $T_C = 25^\circ\text{C}$	60	A
$I_C$	Continuous collector current at $T_C = 100^\circ\text{C}$	30	A
$I_{CP}^{(1)}$	Pulsed collector current	120	A
$V_{GE}$	Gate-emitter voltage	$\pm 20$	V
$I_F$	Continuous forward current at $T_C = 25^\circ\text{C}$	60	A
$I_F$	Continuous forward current at $T_C = 100^\circ\text{C}$	30	A
$I_{FP}^{(1)}$	Pulsed forward current	120	A
$P_{TOT}$	Total dissipation at $T_C = 25^\circ\text{C}$	58	W
$V_{ISO}$	Insulation withstand voltage (RMS) from all three leads to external heat sink ( $t = 1\text{ s}$ ; $T_c = 25^\circ\text{C}$ )	3.5	kV
$T_{STG}$	Storage temperature range	- 55 to 150	$^\circ\text{C}$
$T_J$	Operating junction temperature	- 55 to 175	$^\circ\text{C}$

1. Pulse width limited by maximum junction temperature.

**Table 3. Thermal data**

Symbol	Parameter	Value	Unit
$R_{thJC}$	Thermal resistance junction-case IGBT	2.6	$^\circ\text{C}/\text{W}$
$R_{thJC}$	Thermal resistance junction-case diode	3.4	$^\circ\text{C}/\text{W}$
$R_{thJA}$	Thermal resistance junction-ambient	50	$^\circ\text{C}/\text{W}$

## 2 Electrical characteristics

$T_J = 25^\circ\text{C}$  unless otherwise specified.

Table 4. Static characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(\text{BR})\text{CES}}$	Collector-emitter breakdown voltage ( $V_{GE} = 0$ )	$I_C = 2 \text{ mA}$	600			V
$V_{CE(\text{sat})}$	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}, I_C = 30 \text{ A}$		1.85	2.3	V
		$V_{GE} = 15 \text{ V}, I_C = 30 \text{ A}$ $T_J = 125^\circ\text{C}$		2.15		
		$V_{GE} = 15 \text{ V}, I_C = 30 \text{ A}$ $T_J = 175^\circ\text{C}$		2.35		
$V_F$	Forward on-voltage	$I_F = 30 \text{ A}$		2	2.6	V
		$I_F = 30 \text{ A}, T_J = 125^\circ\text{C}$		1.7		V
		$I_F = 30 \text{ A}, T_J = 175^\circ\text{C}$		1.6		V
$V_{GE(\text{th})}$	Gate threshold voltage	$V_{CE} = V_{GE}, I_C = 1 \text{ mA}$	5	6	7	V
$I_{CES}$	Collector cut-off current ( $V_{GE} = 0$ )	$V_{CE} = 600 \text{ V}$			25	$\mu\text{A}$
$I_{GES}$	Gate-emitter leakage current ( $V_{CE} = 0$ )	$V_{GE} = \pm 20 \text{ V}$			250	nA

Table 5. Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$C_{ies}$	Input capacitance	$V_{CE} = 25 \text{ V}, f = 1 \text{ MHz}, V_{GE} = 0$	-	3750	-	pF
$C_{oes}$	Output capacitance		-	120	-	pF
$C_{res}$	Reverse transfer capacitance		-	77	-	pF
$Q_g$	Total gate charge	$V_{CC} = 480 \text{ V}, I_C = 30 \text{ A}, V_{GE} = 15 \text{ V}$ , see <a href="#">Figure 28</a>	-	163	-	nC
$Q_{ge}$	Gate-emitter charge		-	28	-	nC
$Q_{gc}$	Gate-collector charge		-	72	-	nC

**Table 6. IGBT switching characteristics (inductive load)**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 400 \text{ V}, I_C = 30 \text{ A}, R_G = 10 \Omega, V_{GE} = 15 \text{ V}$ , see <a href="#">Figure 27</a>	-	45	-	ns
$t_r$	Current rise time		-	16	-	ns
$(di/dt)_{on}$	Turn-on current slope		-	1500	-	A/ $\mu\text{s}$
$t_{d(off)}$	Turn-off delay time		-	189	-	ns
$t_f$	Current fall time		-	19	-	ns
$E_{on}^{(1)}$	Turn-on switching losses		-	383	-	$\mu\text{J}$
$E_{off}^{(2)}$	Turn-off switching losses		-	233	-	$\mu\text{J}$
$E_{ts}$	Total switching losses		-	616	-	$\mu\text{J}$
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 400 \text{ V}, I_C = 30 \text{ A}, R_G = 10 \Omega, V_{GE} = 15 \text{ V}, T_J = 175 \text{ }^\circ\text{C}$ , see <a href="#">Figure 27</a>	-	42	-	ns
$t_r$	Current rise time		-	17	-	ns
$(di/dt)_{on}$	Turn-on current slope		-	1337	-	A/ $\mu\text{s}$
$t_{d(off)}$	Turn-off delay time		-	193	-	ns
$t_f$	Current fall time		-	32	-	ns
$E_{on}^{(1)}$	Turn-on switching losses		-	794	-	$\mu\text{J}$
$E_{off}^{(2)}$	Turn-off switching losses		-	378	-	$\mu\text{J}$
$E_{ts}$	Total switching losses		-	1172	-	$\mu\text{J}$

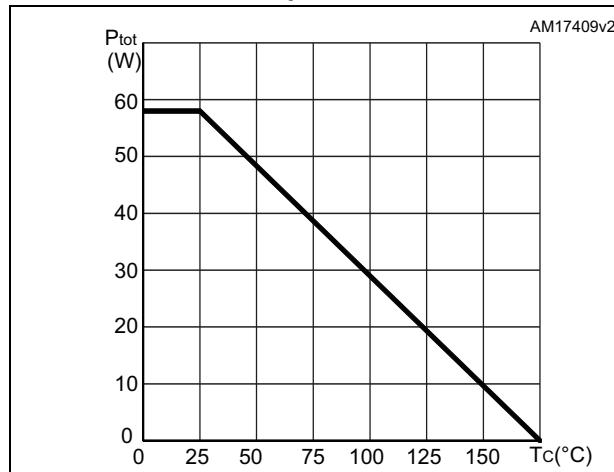
1. Energy losses include reverse recovery of the diode.
2. Turn-off losses include also the tail of the collector current.

**Table 7. Diode switching characteristics (inductive load)**

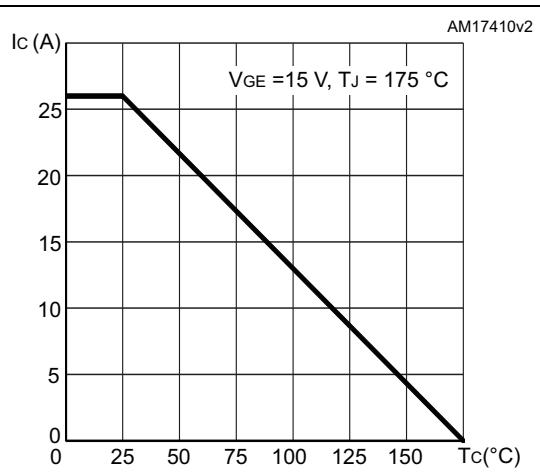
Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{rr}$	Reverse recovery time	$I_F = 30 \text{ A}, V_R = 400 \text{ V}, di/dt=1000 \text{ A}/\mu\text{s}, V_{GE} = 15 \text{ V}$ , (see <a href="#">Figure 27</a> )	-	53	-	ns
$Q_{rr}$	Reverse recovery charge		-	384	-	nC
$I_{rrm}$	Reverse recovery current		-	14.5	-	A
$dI_{rr}/dt$	Peak rate of fall of reverse recovery current during $t_b$		-	788	-	A/ $\mu\text{s}$
$E_{rr}$	Reverse recovery energy		-	104	-	$\mu\text{J}$
$t_{rr}$	Reverse recovery time	$I_F = 30 \text{ A}, V_R = 400 \text{ V}, di/dt=1000 \text{ A}/\mu\text{s}, V_{GE} = 15 \text{ V}, T_J = 175 \text{ }^\circ\text{C}$ , (see <a href="#">Figure 27</a> )	-	104	-	ns
$Q_{rr}$	Reverse recovery charge		-	1352	-	nC
$I_{rrm}$	Reverse recovery current		-	26	-	A
$dI_{rr}/dt$	Peak rate of fall of reverse recovery current during $t_b$		-	310	-	A/ $\mu\text{s}$
$E_{rr}$	Reverse recovery energy		-	407	-	$\mu\text{J}$

## 2.1 Electrical characteristics (curves)

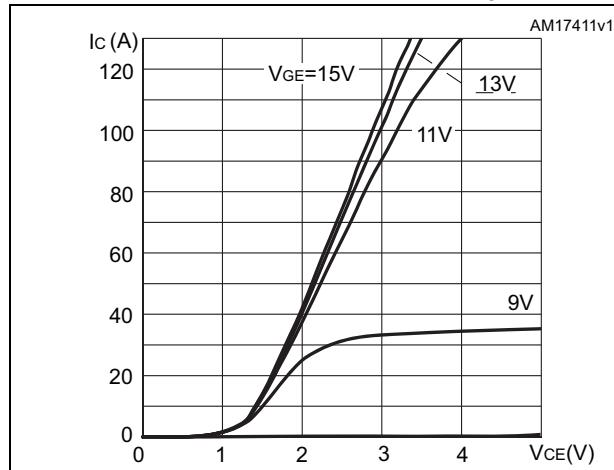
**Figure 2. Power dissipation vs. case temperature**



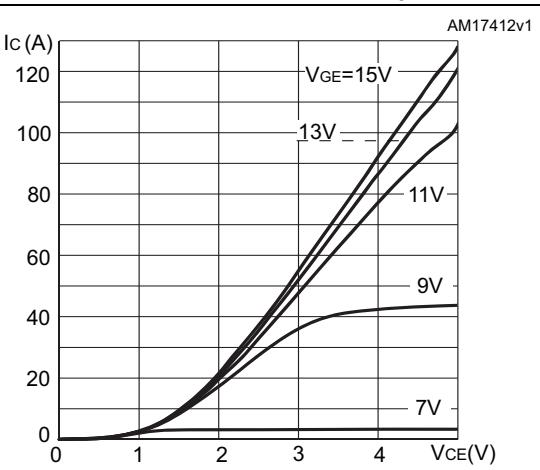
**Figure 3. Collector current vs. case temperature**



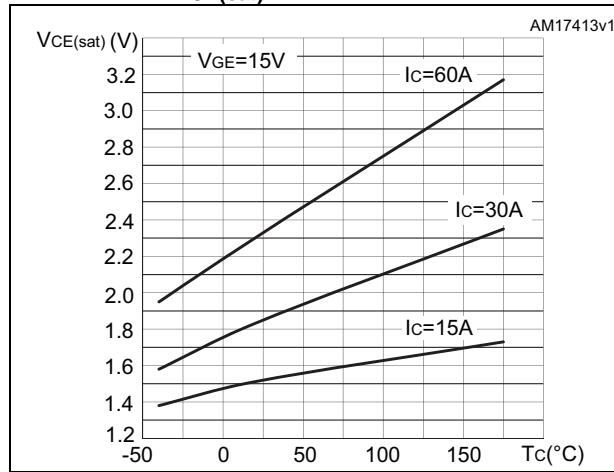
**Figure 4. Output characteristics ( $T_j=25^\circ\text{C}$ )**



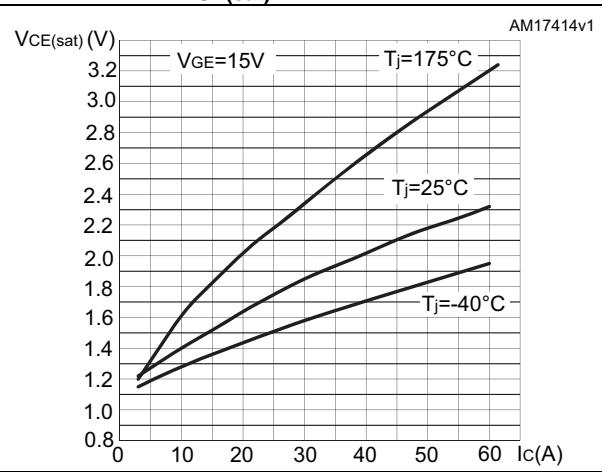
**Figure 5. Output characteristics ( $T_j=175^\circ\text{C}$ )**

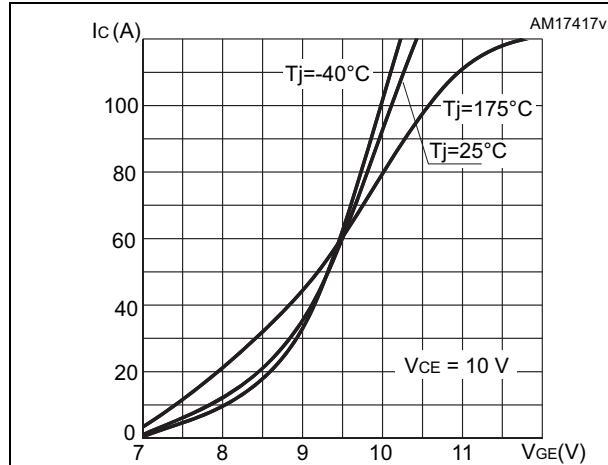
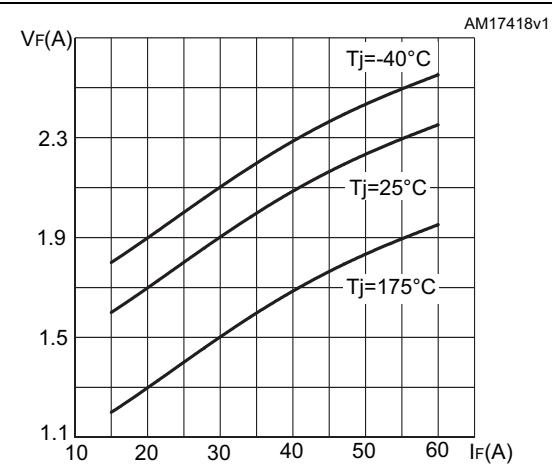
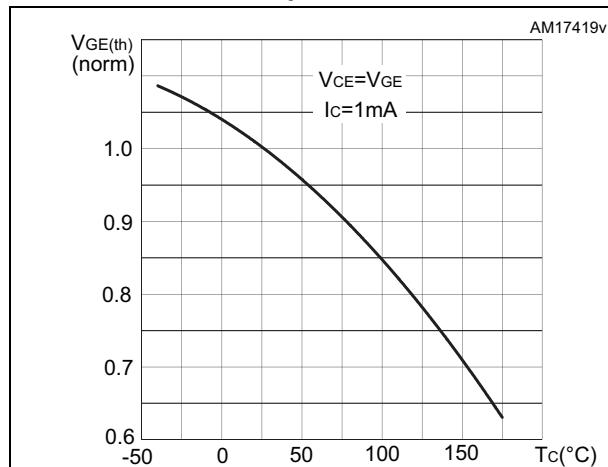
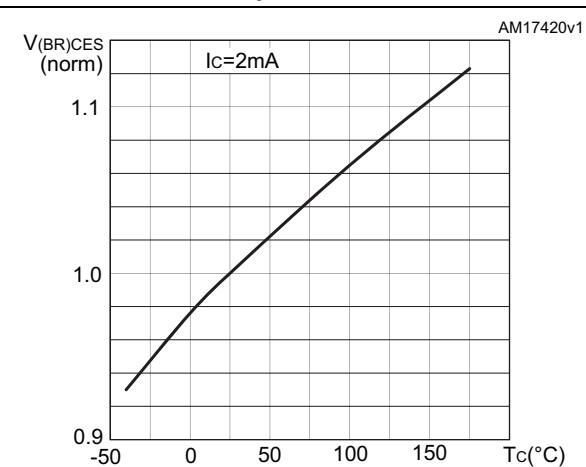
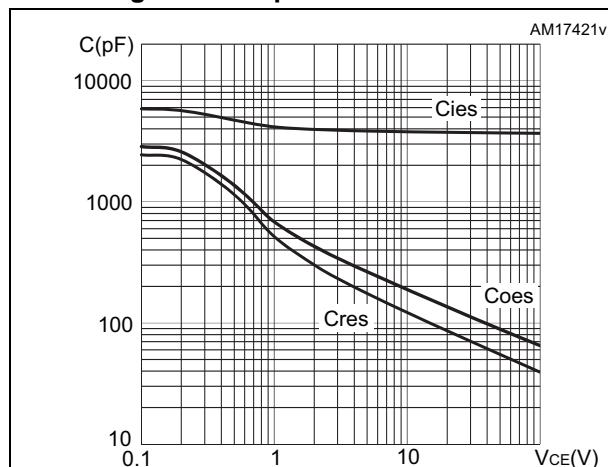
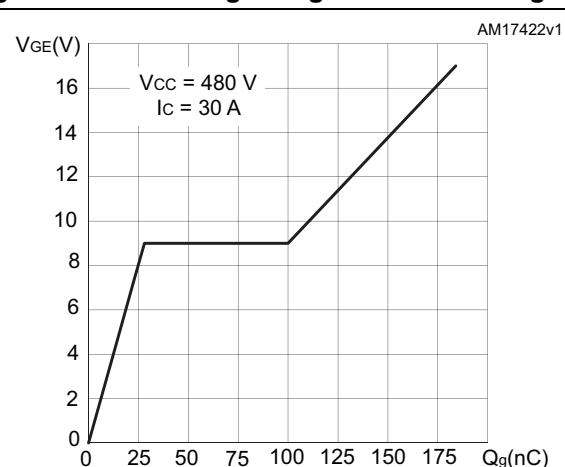


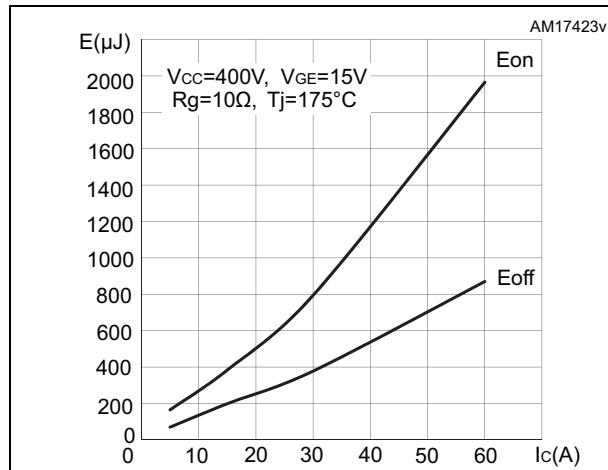
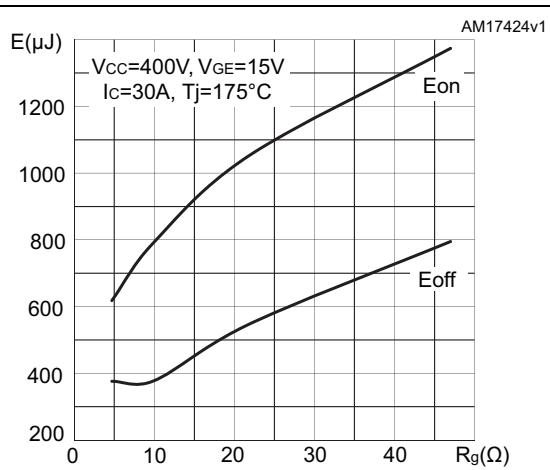
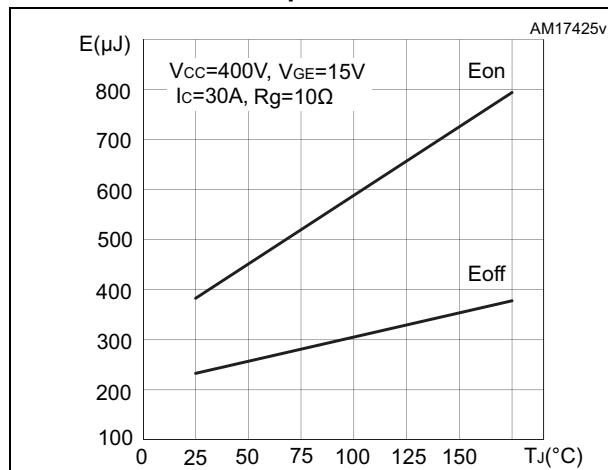
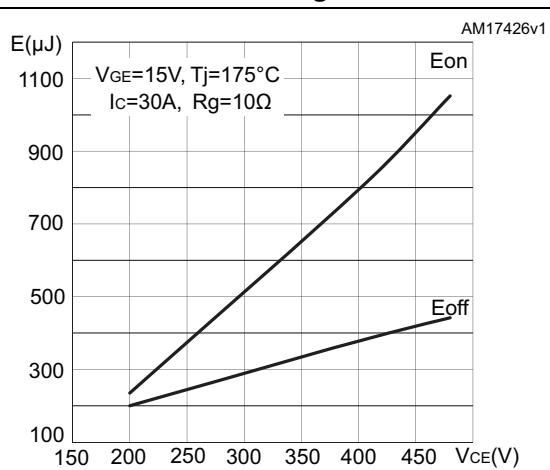
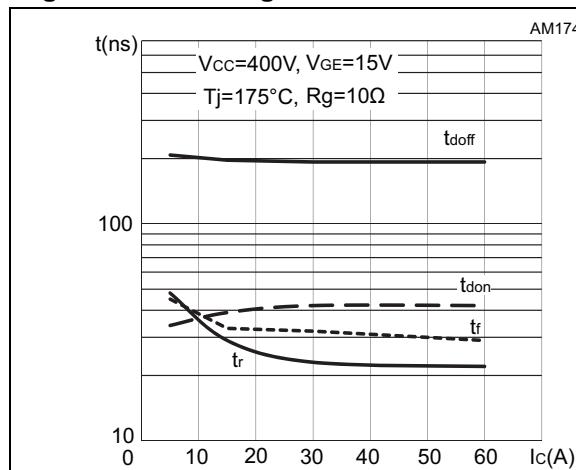
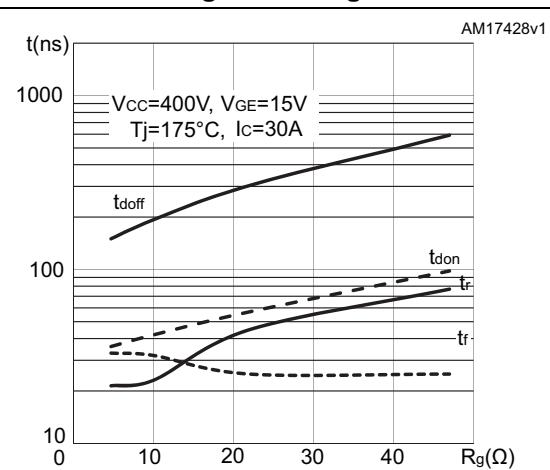
**Figure 6.  $V_{CE(\text{sat})}$  vs. junction temperature**



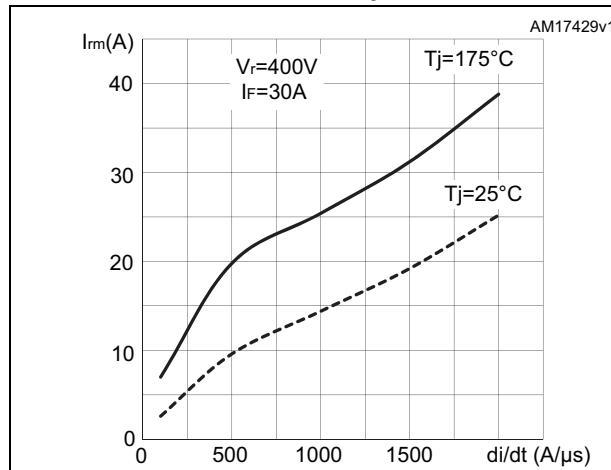
**Figure 7.  $V_{CE(\text{sat})}$  vs. collector current**



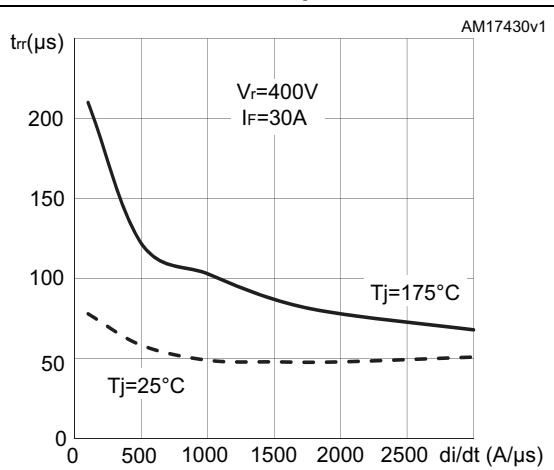
**Figure 8. Transfer characteristics****Figure 9. Diode  $V_F$  vs. forward current****Figure 10. Normalized  $V_{GE(th)}$  vs junction temperature****Figure 11. Normalized  $V_{(BR)CES}$  vs. junction temperature****Figure 12. Capacitance variations****Figure 13. Gate charge vs. gate-emitter voltage**

**Figure 14. Switching losses vs. collector current****Figure 15. Switching losses vs. gate resistance****Figure 16. Switching losses vs. junction temperature****Figure 17. Switching losses vs. collector emitter voltage****Figure 18. Switching times vs. collector current****Figure 19. Switching times vs. gate resistance**

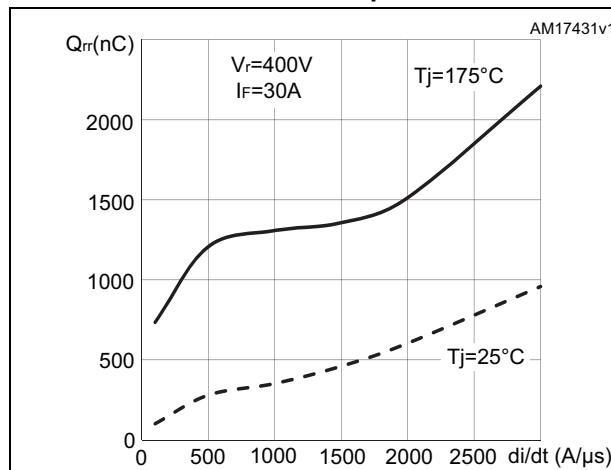
**Figure 20. Reverse recovery current vs. diode current slope**



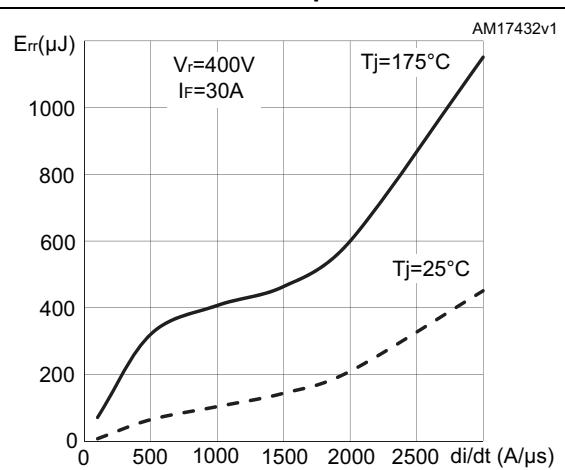
**Figure 21. Reverse recovery time vs. diode current slope**



**Figure 22. Reverse recovery charge vs. diode current slope**



**Figure 23. Reverse recovery energy vs. diode current slope**



**Figure 24. Safe operating area**

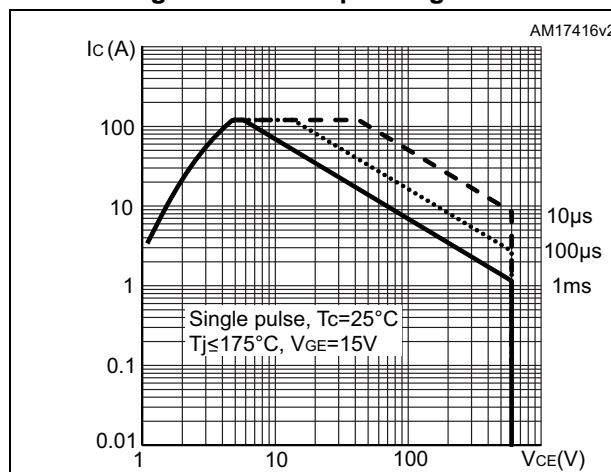


Figure 25. Thermal data for IGBT

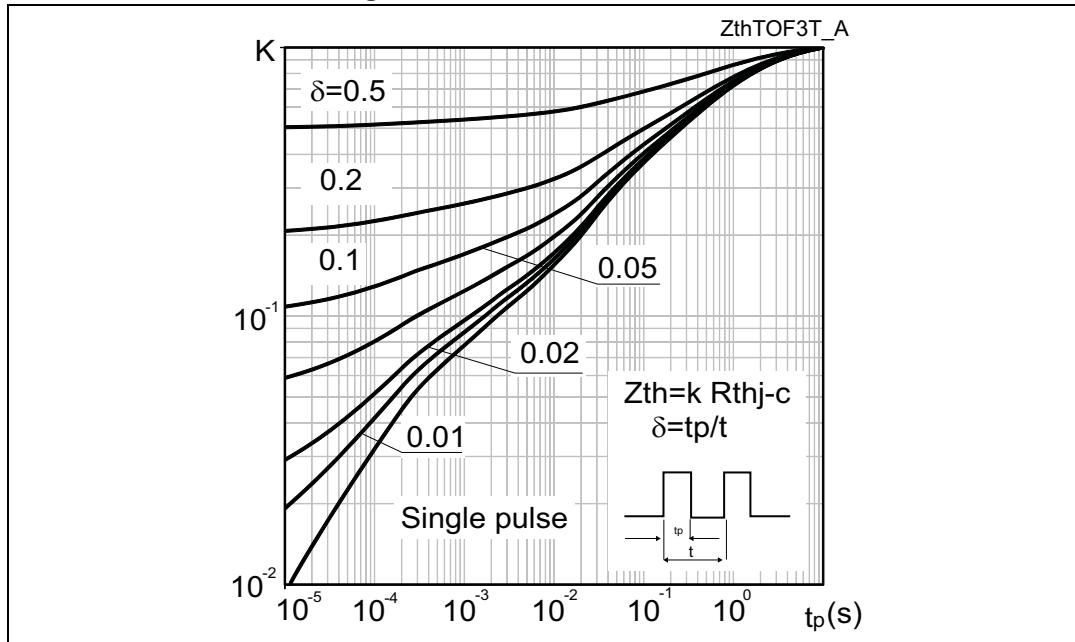
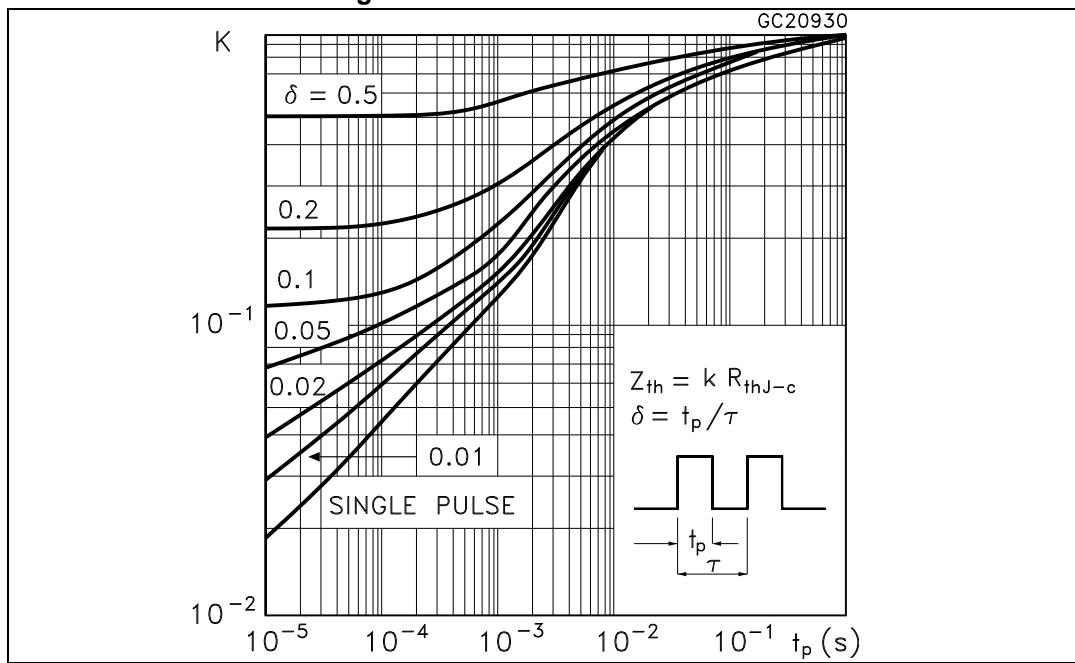
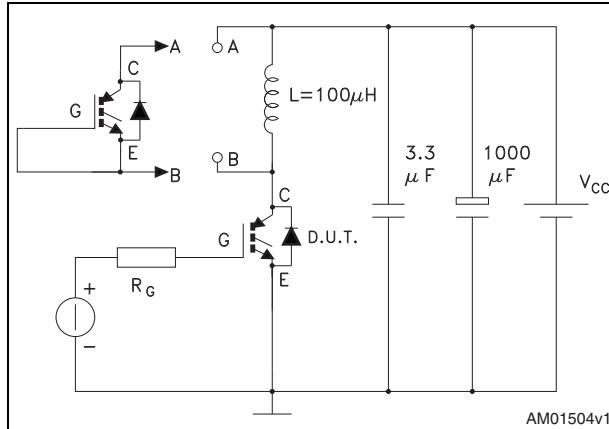


Figure 26. Thermal data for diode

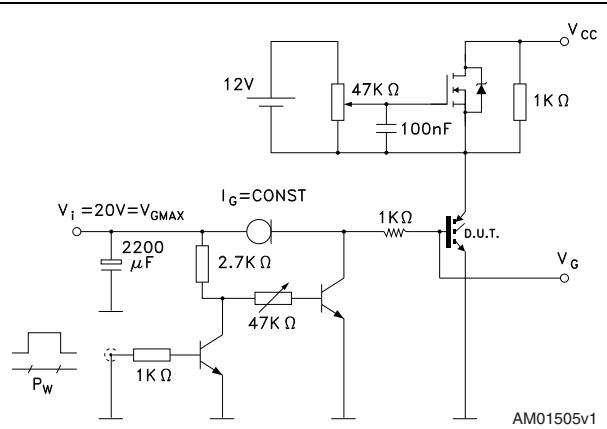


### 3 Test circuits

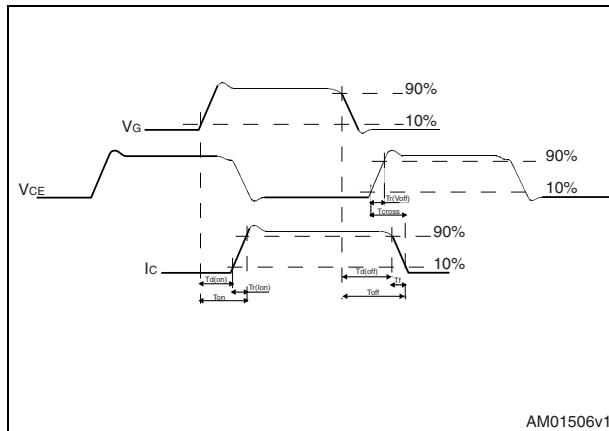
**Figure 27. Test circuit for inductive load switching**



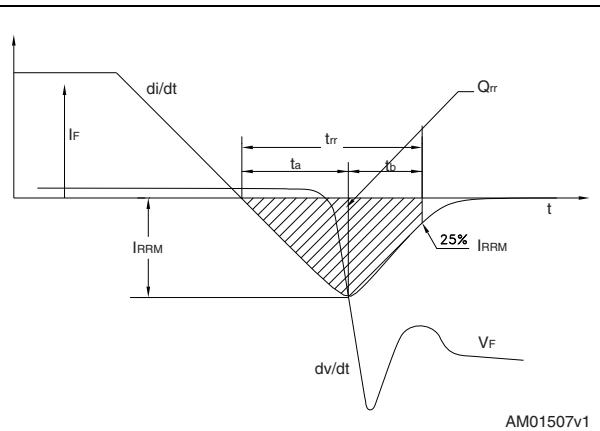
**Figure 28. Gate charge test circuit**



**Figure 29. Switching waveform**



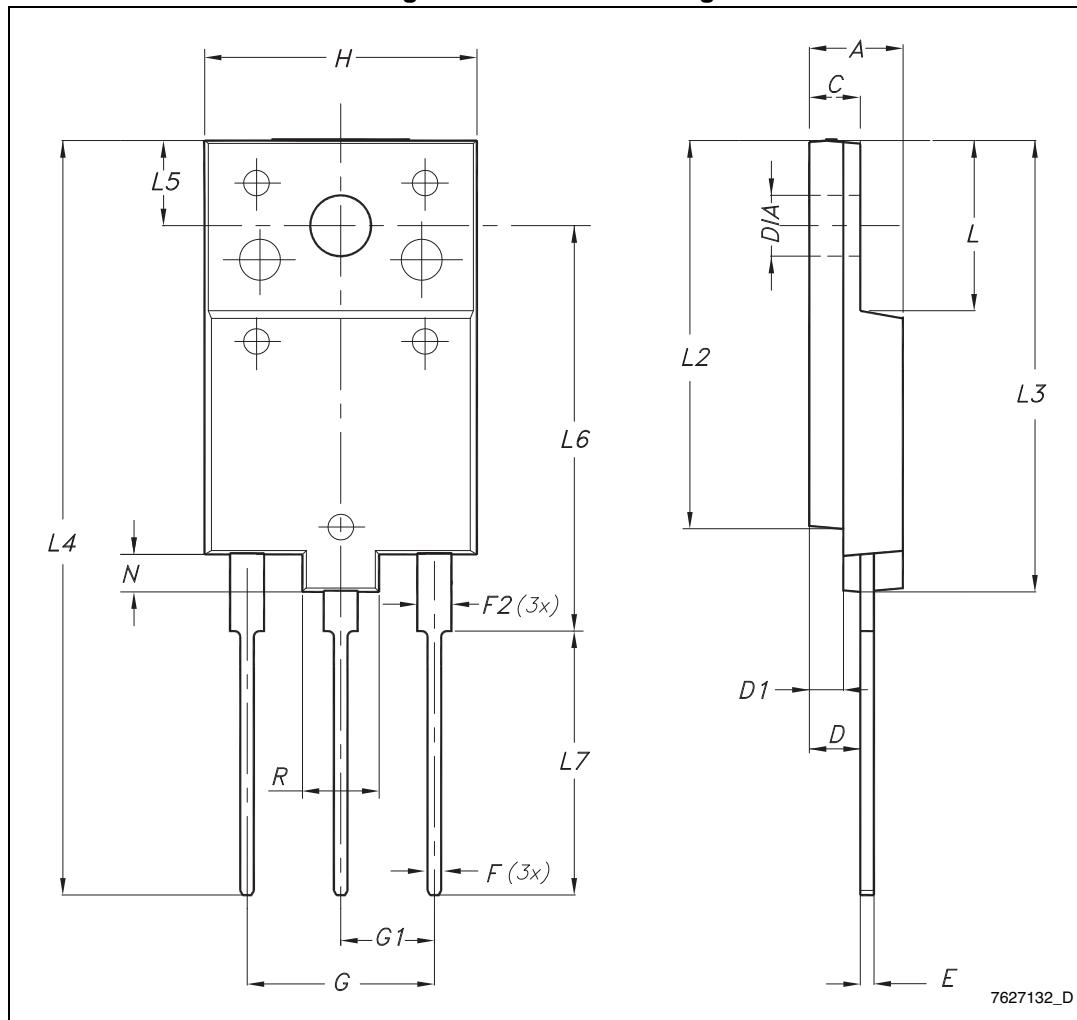
**Figure 30. Diode recovery time waveform**



## 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com).  
ECOPACK is an ST trademark.

Figure 31. TO-3PF drawing



**Table 8. TO-3PF mechanical data**

Dim.	mm		
	Min.	Typ.	Max.
A	5.30		5.70
C	2.80		3.20
D	3.10		3.50
D1	1.80		2.20
E	0.80		1.10
F	0.65		0.95
F2	1.80		2.20
G	10.30		11.50
G1		5.45	
H	15.30		15.70
L	9.80	10	10.20
L2	22.80		23.20
L3	26.30		26.70
L4	43.20		44.40
L5	4.30		4.70
L6	24.30		24.70
L7	14.60		15
N	1.80		2.20
R	3.80		4.20
Dia	3.40		3.80

## 5 Revision history

**Table 9. Document revision history**

Date	Revision	Changes
31-Mar-2014	1	Initial release.

**Please Read Carefully:**

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

**UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.**

**ST PRODUCTS ARE NOT DESIGNED OR AUTHORIZED FOR USE IN: (A) SAFETY CRITICAL APPLICATIONS SUCH AS LIFE SUPPORTING, ACTIVE IMPLANTED DEVICES OR SYSTEMS WITH PRODUCT FUNCTIONAL SAFETY REQUIREMENTS; (B) AERONAUTIC APPLICATIONS; (C) AUTOMOTIVE APPLICATIONS OR ENVIRONMENTS, AND/OR (D) AEROSPACE APPLICATIONS OR ENVIRONMENTS. WHERE ST PRODUCTS ARE NOT DESIGNED FOR SUCH USE, THE PURCHASER SHALL USE PRODUCTS AT PURCHASER'S SOLE RISK, EVEN IF ST HAS BEEN INFORMED IN WRITING OF SUCH USAGE, UNLESS A PRODUCT IS EXPRESSLY DESIGNATED BY ST AS BEING INTENDED FOR "AUTOMOTIVE, AUTOMOTIVE SAFETY OR MEDICAL" INDUSTRY DOMAINS ACCORDING TO ST PRODUCT DESIGN SPECIFICATIONS. PRODUCTS FORMALLY ESCC, QML OR JAN QUALIFIED ARE DEEMED SUITABLE FOR USE IN AEROSPACE BY THE CORRESPONDING GOVERNMENTAL AGENCY.**

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2014 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

[www.st.com](http://www.st.com)

