HI3312X101R-10

UNCONTROLLED DOCUMENT

PHYSICAL DIMENSIONS:

A 8.50 [.335]	± 0.20 [.008]
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- B 3.05 [.120] ± 0.20 [.008]
- ± 0.20 [.008] C 2.28 [.090]
- D 0.89 [.035] ± 0.20 [.008]



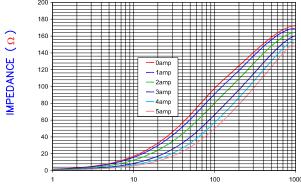
Z @ 100M (Ω)	1Hz	DCR (Ω)	Rated Current
Nominal	100		
Minimum	75		
Maximum	125	0.004	10,000 mA

ELECTRICAL CHARACTERISTICS:

NOTES: UNLESS OTHERWISE SPECIFIED

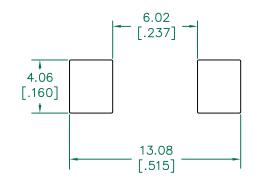
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS,13" REELS, 2,500 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)

LAND PATTERNS FOR REFLOW SOLDERING

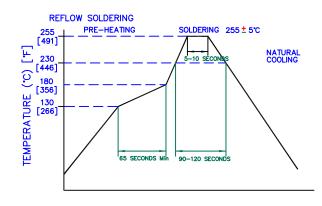


(For wave soldering, add 0.762 [.030] to this dimension.)

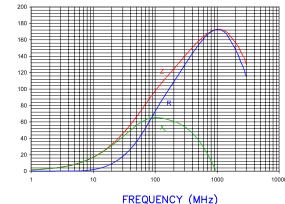
Α

REV

RECOMMENDED SOLDERING CONDITIONS



|Z| , R, AND X vs. FREQUENCY



FREQUENCY (MHz)

AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3435

IMPEDANCE



	DIMENSIONS ARE IN mm [INCHE	This print is the prope		
				Tech. and is loaned in subject to return upon
				with the understanding
				copies shall be made w written consent of Laird
Ε	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	rights to design or inve reserved.
D	UPDATE COMPANY LOGO	02/11/08	JRK	PROJECT/PART NUMBER:
С		08/29/06		HI3312X101I
В	INCREASE "D" DIMENSION TO 0.35	03/20/06	JRK	DATE: 09 /05 /13

ORIGINAL DRAFT

DESCRIPTION

09/19/05 JRK

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08/05/13

"HI3312X101R-10-E



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CO-FIRE SHEET:

2 of 2

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