

Precision Differential Fanout Buffers Selector Guide

Microsemi high-performance buffers deliver industry leading power supply noise rejection performance and low additive jitter. This preserves signal integrity resulting in high performance while simplifying engineering board design efforts.

Precision Differential Fanout Buffers

Product	Output Type	Inputs	Outputs	Input Termination	Switching	750 MHz Additive Jitter fs RMS typ	Input Type	Input Coupling	Operating Frequency	Power Supply (V)	Operating Temp (°C)	Pkg	Pkg Size (mm)	
ZL40200	LVPECL	1	2	External	N/A	30–40	LVPECL LVDS HCSL CML	DC or AC	Up to 750 MHz	2.5 or 3.3	–40 to 85	QFN-16	3 x 3	
ZL40201				Internal										
ZL40202			4	External										
ZL40203				Internal										
ZL40204			6	External										
ZL40205				Internal										
ZL40206			8	External										Simple
ZL40207				Internal										
ZL40224		2	6	External	Glitch Free	106–121								
ZL40225				Internal										
ZL40208				8								External		
ZL40209												Internal		
ZL40210				8								External		
ZL40211												Internal		
ZL40212	LVDS	1	2	External	N/A	78–138	DC or AC	Up to 750 MHz	2.5 or 3.3	–40 to 85	QFN-16	3 x 3		
ZL40213				Internal										
ZL40214			4	External										
ZL40215				Internal										
ZL40216			6	External										
ZL40217				Internal										
ZL40218			8	External									Simple	
ZL40219				Internal										
ZL40226		2	6	External	Glitch Free	165–194								
ZL40227				Internal										
ZL40220				8							External			
ZL40221											Internal			
ZL40222				8							External			
ZL40223	Internal													

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Microsemi miSmartBuffer™ family provides 3-, 6-, or 10-output programmable fanout buffers with multi-format I/O and per-output dividers. Applications include clock signal fanout, format conversion, frequency division, and skew adjustment in a wide variety of equipment types.

Precision Differential miSmartBuffers

Product	Input Type	Input Freq.	Crystal Driver	Diff Outputs LVPECL/LVDS/HCSL	CMOS Outputs	Output Banks	Divider Value	Per-output Enable	Per-output Phase Adjust	OE Control	Host Bus	Internal Memory	Operating Temp (°C)	Pkg	Pkg Size (mm)
ZL40250	3 Diff/ LVCMOS + Crystal	<1 GHz	•	0–6	0–12	6	32-bit	•	•	Per output by GPIO	SPI/I2C	ROM	–40 to 85	QFN-56	8 x 8
ZL40251				EEEPROM											
ZL40252				0–10	0–20	ROM									
ZL40253				EEEPROM											
ZL40255				0–3 400 mV/800 mV CML	0–6	3						QFN-32			



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