# **SPECIFICATION** SPEC. No. DATE: То For Automotive CUSTOMER'S PRODUCT NAME TDK'S PRODUCT NAME Ceramic insulated capacitors disc type for low dissipation (RR type) TYPE: CK45-R3△DOOOK A♦RA RECEIPT CONFIRMATION

**TDK** Corporation

Sales

Engineering

DATE:

Electronic Components Sales &

Ceramic Capacitors Business Group .

Marketing Group

APPROVED	Person in charge

CHECKED	Person in charge
	CHECKED

YEAR

MONTH

DAY

# Handling precautions for High voltage ceramic capacitors

Please read the following closely before using these products.

# Safety precautions

The following precautions should be observed strictly to ensure safety design. Misuse of the product may lead to smoking of the product.

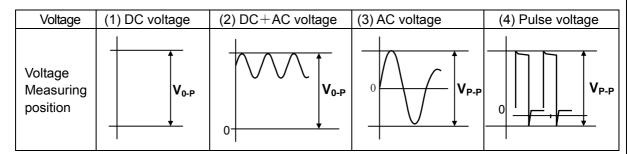


#### 1. Operating voltage

Use within the rated voltage of capacitor between terminals. For DC rated voltage application, you should control the peak voltage (Vo-p) under the rated voltage in case the AC voltage is superimposed on the DC voltage. Use within the rated voltage includes peak voltage (Vp-p) when AC voltage or impulse voltage applied in a circuit. Confirm irregular voltage (surge voltage, static electricity, switching noise, etc) occurs in the equipment used, and use within the rated voltage containing the irregular voltage.

When the capacitor is used as a noise suppressor in the AC primary circuit, the voltage proof test should be within the specified conditions (voltage, time, wave form, etc).

Connect by confirmation of non lose contact, and the voltage is started to apply to the circuit from zero to the specified voltage and it is stopped applying from the voltage to zero.



#### 2. Operating temperature

Be sure to use only those operating temperature described in our catalogue or specification. Keep the surface temperature under the maximum temperature, which includes the maximum self-heat temperature of 20 degree C.

#### 3. Self-exothermal

Self-exothermal temperature should be within 20 degree C on the condition of atmosphere temperature 25 degree C without the influence of wind such as the cooling fan. Be sure to use a capacitor in a circuit of current increase by AC voltage or pulse voltage applied.

When high frequency voltage or impulse voltage applied in a circuit, reliability should be influenced. Take into considerations the load reduction and self-exothermal temperature, even if voltage should be within the rated voltage.

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#### 4. Capacitance change of capacitors

For some of the capacitors, capacitance value may change considerably in the temperature range, or by applied DC voltage. And capacitor has aging characteristic (capacitance decreases by keeping as it is). When you use the capacitor in the time constant circuit, consult TDK whether the capacitor is available or not.

#### 5. Vibration of capacitors

When the capacitor class 2 is used in the AC circuit, or pulse circuit, the capacitor might vibrate or noise might occur in the specified frequency. Be sure to confirm the conditions before using the capacitor.

#### 6. Usage of capacitance and storage

Don't use capacitors in the following environments:

- \* Direct sunshine
- \* Areas directly exposed to water or salty water
- \* Areas that become dewy
- \* Areas filled with toxic gases (such as hydrogen sulfide, sulfur dioxide, chlorine, ammonia, etc)
- \* Areas exposed to excess vibrations or shock conditions described in our catalogue or specification. Store capacitors in an environment from -10 to 40 degree C, with 15 to 70%RH for 6 months maximum and use within the period after receiving the capacitors.

#### 7. Inserting precautions

When inserting capacitors into the PC board by automatic insertion machine, confirm the conditions (such as pressure of pusher, adjustment of clinching portion) and minimize the impact force by chucking the body, or clinching the lead terminals.

Distances between the hole position onto a PC board should be equal to the pitch of capacitors. When stretching the lead terminal, any force may load the bottom of the capacitor body and result in damage to the insulation coating. Severe damages may cause poor reliability.

#### 8. Soldering

Don't immerse the capacitor body into the molten solder, and don't solder the terminals by reflow soldering. Use PC board, and solder the terminals in the opposite side of the body. Soldering conditions, such as pre-heat temperature, soldering temperature, and soldering time, should be followed by the descriptions in our catalogue or specification. (refer to Fig.-1)

Adjust the amount of solder within the proper volume. Select an appropriate soldering material. When using soldering iron for installing capacitors or reworking onto the PC board, sufficient pre-heating and temperature control should be used. We recommend that the iron condition is  $350\pm10$  degree C/  $3.5\pm0.5s$ . as 1 time, and you should use an adequate tip diameter ( $\phi$ 3mm Max.) with the soldering iron as well as a proper wattage (50W Max.). Don't touch the capacitor body directly with soldering tip, except for the terminals of capacitor.

#### 9. Flux

When using flux for soldering capacitors onto the PC board, spread it thinly and uniformly. Flux will be composed of halogenated material less than 0.1 wt% (cl conversion). Don't use a strong acid grade of flux. When using water-soluble flux, sufficient cleansing should be done.

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#### 10. Cleansing

When the cleansing should not be sufficient, the cleansing liquid or any residue might leave on the capacitor body, they may deteriorate the insulation coating or performance (insulation resistance, etc.). When using ultrasonic cleansing, avoid transmitting vibrations onto the PC board. Conditions of ultrasonic cleansing, such as output frequency and time of the method, should be taken into considerations.

After cleansing capacitors, dry them well. Cleansing liquid should not contain electrolyte, nor leave any residue. Through the result of the cleansing method, confirm whether the quality of the capacitors have been affected due to the conditions.

#### 11. Coating or molding

When coating or molding capacitors after installing components onto the PC board, confirm whether the performance of capacitors may not be damaged by the work.

#### 12. Mechanical stress

Don't submit to excessive mechanical shock. Don't use capacitors which may have been damaged due to dropping, etc.

If possible, avoid bending the terminals of capacitors. In an unavoidable case of bending, use a small jig to decrease the mechanical stress on the capacitors.

#### 13. Others

Please contact TDK before using our capacitors listed in this catalogue or specifications for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property, or when intending to use one of our capacitors for other applications than specified in this catalog or specifications.

- \* Medical equipment
- \* Aerospace equipment
- \* Power plant equipment
- \* Aircraft equipment
- \* Transportation equipment (trains, ships, etc)
- \* Undersea equipment
- \* Traffic signal equipment
- \* Disaster prevention, crime prevention equipment
- \* Data processing equipment exerting influence on public
- \* Application of similar complexity and, or reliability requirements to the applications listed in the above.

Please refer to the guideline of notabilia for fixed ceramic capacitors issued by JEITA (Japan Electronics and Information Technology Association, EIAJ RCR-2335).

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#### Scope

This specification applies to ceramic insulated capacitors disc type used in electronic equipment.

#### Relative standards

JIS C 6422-1991

JIS C 5102-1994

#### Mention item

- 1. Part No.
- 2. Operating temperature range
- 3. Test condition
- 4. Performance
- 5. Marking
- 6. Figure & Dimension
- 7. Label, Packing & Transport
- 8. Notification before the modification

We do not use the following material (1),(2) in these products.

- (1) PBBOs (Poly Bromo Biphenyl Oxides)
- (2) PBBs (Poly Bromo Biphenyis)

We do not use Class I ODS (Ozone depleting substances ) in all our process of these products.

These products shall conform to RoHS Directive.

These products are Halogen-free.(Br≦900ppm, Cl≦900ppm, Br+Cl≦1500ppm)

#### Manufacturing place

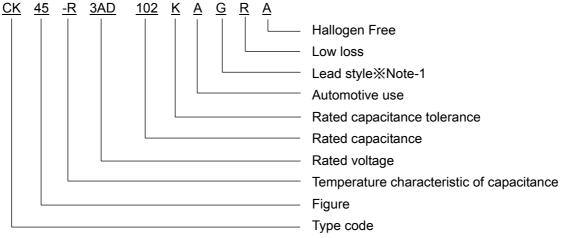
Manufacturing site should be TDK Taiwan & TDK Xiamen

Division	Date Issued	SPEC No.
Ceramic Capacitors Business Group		



#### 1. Part No.

(Example)



※ Note-1

Lead style G: Vertical kink long lead (Bulk)

N : Vertical kink short lead (Bulk)V : Vertical kink long lead (Taping)

- 2. Operating Temperature range: -40 °C to +125 °C X Note-2
  - Note-2 Operating temperature range max. is +125 °C (Including capacitor's self-heating max. +20 °C)

#### 3. Test condition

Test and measurement shall be made at the standard condition, (Temperature 15 to 35  $^{\circ}$ C, relative humidity 45 to 75 % and atmospheric pressure 860 to 1060 hPa.),Unless otherwise specification herein. If doubt occurred on the value of measurement, and remeasurement was requested by customer capacitors shall be measured at the reference condition (Temperature 20 ±2  $^{\circ}$ C ,relative humidity 60 to 70 % and atmospheric pressure 860 to 1060 hPa.)

#### 4. Performance

The performances shall comply with Table-1

Table-1

	Table-1							
No	Ite	ems	Performance		Test method			
1		The appearance and dimension shall be as given in paragraph 6 and Table-3 to 4			Visual check and measuring with Micrometer.			
2	Marking ans resistance	solvent	The marking shall legible (Paragraph	•		20 ∼25 °C al check	/ 30 ±5 sec.	
3	Withstand voltage	Between terminals	No failure		charg or les	ge and disch		shall be 50mA
		Between terminal and exterior cladding	No failure		The metallic small ball method 1300V DC (1 to 5 sec.) Charge and discharge currentshall be 50mA or less.		about 2 mm	
4	Insulation resistance	Between terminals	10000 MΩ or more	•			sistance shou C500±50V wi	ld be thin 60±5 sec.
5	Capacitance Dissipation fa	ector(tanδ)	With the tolerances Table-3 to 4 0.2 % or less	specified with			ency : 1kHz ± e : 5Vrms. or	
	•	` ,		15 0/	The	ana aita a a a	m 0 0 0 1 / m 0 m 0 =	t aboutd be
/			Within -30 % ~ +15 % (at -25°C ~ +125°C)		made temp	e at each ste	measuremen p specified ir ficient on the f Step 3.	n table.
			Step Temp.(°C) 20		1 0±2	2 :	3 4 ±2 125±2	5 20±2
8	Strength of terminal	Tensile strength	Lead wire shall not be disconnected, and capacitor shall not be damaged.		The force of 10N shall be applied to the axia direction of the termination.		lied to the axial	
		Bending strength	Lead wire shall not be disconnected, and capacitor shall not be damaged.		direct inclinathe be positi Furth the of	ion of the tered through a cody shall be on. ermore the barrier direction	rminal and than angle of 90 returned to the body shall be not 90 degrees	inclined to
9	Vibration resistance	Appearance	No marked defect		Vibration frequency range: 10 to 55Hz. Displacement: 0.75mm		to 55Hz.	
		Capacitance change Dissipation factor	Within the tolerances specified with No.5  Within the value specified with		with No.5 (2 hours for each direction: X,Y) Within the value specified with		<b>/</b> ,Z)	
		( tan δ )	No.6					
10	Resistance	Appearance	No marked defect		Soldering temperature:350±10°C/3.5±0.5 s o		0°C/3.5±0.5 s or	
	to soldering heat	Capacitance change	Within ± 5%		260±5°C/10±1 s Dipping depth: 1.5 to 2.0mm from the bottom of lead terminal.		om the bottom	
		Withstand voltage Between terminals	No failure		(shiel Initia Leav	ding board s l :pre-heat 1 ing room ten	np. for24±2h.	
					Aiter	iesi. ieaviiig	room temp.	IUI Z#IZII.



# Table-1 Continue

	Table-1 Continue					
No.			Performance	Test method		
11	Solderability		At least 3/4 of circumferential dipped into solder shall be covered with new solder.	Soldering temperature: 245± 5 °C Dipping time: 2± 0.5 sec. Concentration of solution shall be about 25 % colophonium in weight ratio		
12	Heat shock test   Appearance		No marked defect	Test condition: (-55°C/30min.←→+125°C/30min.) *1000 cycles.		
13	Moisture resistance	Appearance	No marked defect	Test temperature : 85 ±2 °C Relative humidity : 80 to 85 %RH		
	(Steady state)	Capacitance change	Within ± 10%	Test time: 1000 +24, -0 hours  Capacitors shall be measured after leaving		
		Dissipation factor ( tan δ )	1.0 % or less	it under room temperature for 1 to 2 hours.		
		Insulation resistance	1000 MΩ or more			
14	High	Appearance	No marked defect	Test temperature : 125± 3 °C		
temperature Capacitance Within± 10% change	Within± 10%	Test time: 1000 +48, -0 hours 150% rated voltage applied.				
		Dissipation factor ( tan δ )	0.4 % or less	Capacitors shall be measured after leaving it under room temperature for 1 to 2 hours. Charge and discharge current shall be		
		Insulation resistance	2000 MΩ or more	50mA or less.		
15	Moisture	Appearance	No marked defect	Test temperature : 85 ±2 °C		
	resistance loading	Capacitance change	Within ± 10%	Relative humidity: 80 to 85 %RH Test time: 1000 +24, -0 hours		
		Dissipation factor ( tan δ )	1.0 % or less	rated voltage applied. Capacitors shall be measured after leaving it under room temperature for 1 to 2 hours. Charge and discharge current shall be		
		Insulation resistance	500 MΩ or more	50mA or less.		
16	Impact test	Appearance	No marked defect	Waveform : Half-sine		
	-	Capacitance change	Within ± 10%	Acceleration : 100G Time:6msec		
		Insulation resistance	2000 M $\Omega$ or more	X, Y, Z, and reverse direction. 3 times each		
		Withstand Voltage	No failure	Capacitor is fixed by resin.		

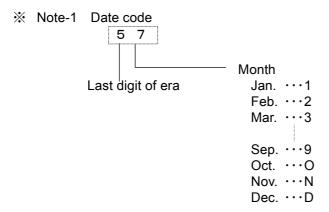


# 5. Marking

Marking on the one side.

Marking item	Example	
1. Low loss 2. T.C 3. Rated capacitance 4. Tolerance of capacitance 5. Rated voltage 6. Date code 7. Automotive use	: R : R : 471 : K : 1kV : 57	RR 471K 1KV <u>57</u>

Tolerance of capacitance K: ±10 %

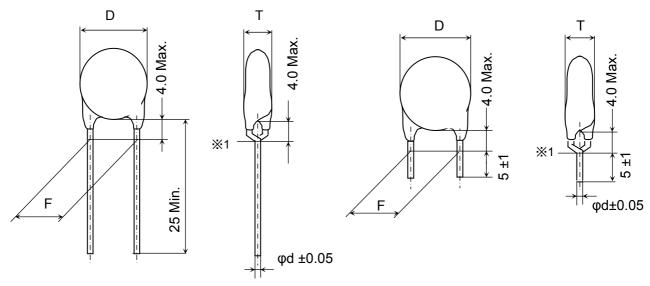


Note-2 Mark color : Black or nearly colors

# 6. Figure & dimension

6.1 Vertical kink long lead (Lead style: G /Bulk)

6.2 Vertical kink short lead (Lead style: N / Bulk)

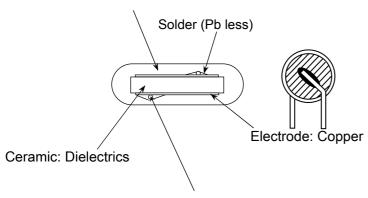


※1 Coating on lead shall not extend beyond the bottom of vertical kink.

Unit: mm

Coating material: Epoxy resin (Color: Blue)

(Flame class): UL94, V-0



Lead wire: CP wire (Tin plated copper covers steel wire, Pb less)



# 6.3 Vertical kink long lead (lead style: V / Taping)

(F=5.0mm, Pitch: 12.7mm, Shape: A, at Table-3~4)

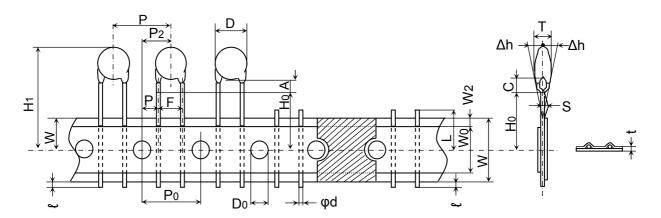
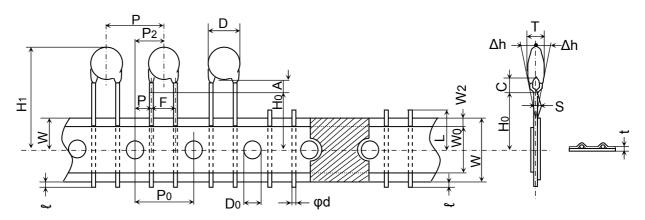


Table-2 Unit: mm

Item			
Name	Symbol	Dimension	Remarks
Body diameter	D	Table-3∼4	
Body thickness	Т	Table-3∼4	
Lead-wire diameter	φd	0.6 ±0.05	
Pitch of component	Р	12.7 ±1.0	Including the slant of body
Feed hole pitch	$P_0$	12.7 ±0.3	Excepting the tape splicing part
Feed hole center to lead	$P_1$	3.85 ±0.7	
Feed hole center to component	$P_2$	6.35 ±1.3	Including the slanting body due to bending lead-wire
Lead-to lead distanc	F	5.0 +0.8,-0.2	Measuring point is bottom kink
Component alignment, F-R	∆h	0 ± 2.0	Including the slanting body due to bending lead-wire
Tape width	W	18.0 +1.0,-0.5	
Adhesive tape width	$\mathbf{W}_{0}$	10.0 Min.	
Hole position	$W_{\scriptscriptstyle 1}$	9.0 ±0.5	
Adhesive tape position	$W_2$	4.0 Max.	Adhesive tape do not stick out the tape
Bottom of kink from tape center	Ho	16.0 +1.5,-0.5	
Height of body from tape center	H <sub>1</sub>	46.0 Max.	
Lead-wire protrusion	l	1.0 Max.	
Feed hole diameter	Do	4.0 ±0.2	
Total tape thickness	t	0.6 ±0.3	Including adhesive tape
Length of snipped lead	L	11.0 Max.	
Coating on lead	С	4.0 Max.	
Height of kink	Α	4.0 Max.	Measuring point is bottom of kink
Spring action	S	2.0 Max.	

# 6.4 Vertical kink long lead (lead style: V / Taping)

(F=7.5mm, Pitch: 15.0mm, Shape: B, at Table- $3\sim$ 4)

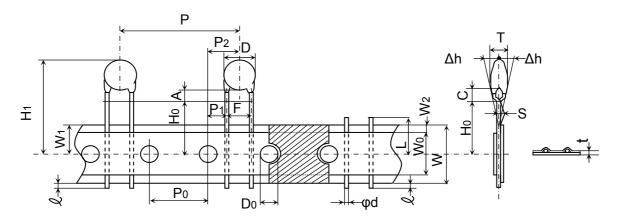


# Table-2 Continue

Unit: mm

		Table-2 Contil	or it is the state of the state
Item Name	Symbol	Dimension	Remarks
Body diameter	D	Table-3∼4	
Body thickness	Т	Table-3∼4	
Lead-wire diameter	φd	0.6 ±0.05	
Pitch of component	Р	15.0 ±1.0	Including the slant of body
Feed hole pitch	$P_0$	15.0 ±0.3	Excepting the tape splicing part
Feed hole center to lead	$P_1$	3.75 ±0.7	
Feed hole center to component	$P_2$	7.5±1.3	Including the slanting body due to bending lead-wire
Lead-to lead distanc	F	7.5±0.8	Measuring point is bottom kink
Component alignment, F-R	Δh	0 ± 2.0	Including the slanting body due to bending lead-wire
Tape width	W	18.0 +1.0,-0.5	
Adhesive tape width	$W_{0}$	10.0 Min.	
Hole position	$W_1$	9.0 ±0.5	
Adhesive tape position	$W_2$	4.0 Max.	Adhesive tape do not stick out the tape
Bottom of kink from tape center	Ho	16.0 +1.5,-0.5	
Height of body from tape center	$H_1$	46.0 Max.	
Lead-wire protrusion	ł	1.0 Max.	
Feed hole diameter	$D_0$	4.0 ±0.2	
Total tape thickness	t	0.6 ±0.3	Including adhesive tape
Length of snipped lead	L	11.0 Max.	
Coating on lead	С	4.0 Max.	
Height of kink	Α	4.0 Max.	Measuring point is bottom of kink
Spring action	S	2.0 Max.	

(F=7.5mm,Pitch: 30.0mm, Shape: C, at Table-3 $\sim$ 4)



# Table-2 Continue

Unit: mm

	-		
Item Name	Symbol	Dimension	Remarks
Body diameter	D	Table-3∼4	
Body thickness	Т	Table-3∼4	
Lead-wire diameter	φd	0.6 ±0.05	
Pitch of component	Р	30.0 ±1.0	Including the slant of body
Feed hole pitch	Po	15.0 ±0.3	Excepting the tape splicing part
Feed hole center to lead	$P_1$	3.75 ±0.7	
Feed hole center to component	$P_2$	7.5±1.3	Including the slanting body due to bending lead-wire
Lead-to lead distanc	F	7.5±0.8	Measuring point is bottom kink
Component alignment, F-R	∆h	0 ± 2.0	Including the slanting body due to bending lead-wire
Tape width	W	18.0 +1.0,-0.5	
Adhesive tape width	Wo	10.0 Min.	
Hole position	$W_1$	9.0 ±0.5	
Adhesive tape position	$W_2$	4.0 Max.	Adhesive tape do not stick out the tape
Bottom of kink from tape center	H <sub>o</sub>	16.0 +1.5,-0.5	
Height of body from tape center	$H_1$	46.0 Max.	
Lead-wire protrusion	ł	1.0 Max.	
Feed hole diameter	Do	4.0 ±0.2	
Total tape thickness	t	0.6 ±0.3	Including adhesive tape
Length of snipped lead	L	11.0 Max.	
Coating on lead	С	4.0 Max.	
Height of kink	Α	4.0 Max.	Measuring point is bottom of kink
Spring action	S	2.0 Max.	

Note-1 Use the gummed tape to connect two ends of broken tape.

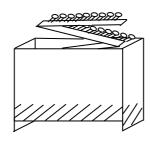
Note-2 Dropouts of parts shall be limited to no more than three consecutive parts.

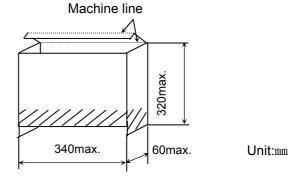
Note-3 Packaging method and dimensions see below.

Note-4 Quantity Pitch: 12.7mm 1000 pcs. /Box.

Pitch: 15.0mm 1000 pcs. /Box. Pitch: 30.0mm 500 pcs. /Box.

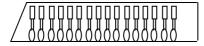
Packaging : Ammo pack





Note-5 Package of shipment

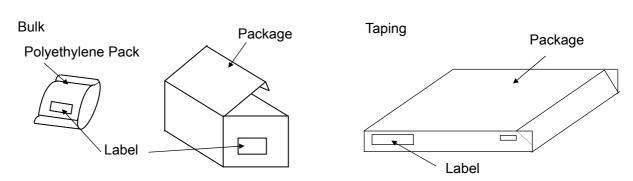
Capacitors pack in downward



#### 7. Labe1 and transport

Capacitors shall be packaged prior to shipment so as to prevent damage during transportation and storage. Shipping carton contains the following information on the label.

- a) TDK item name
- b) Quantity
- c) TDK inspection number
- d) Manufacturer's name
- e) Country of origin



#### 8. Notification before the modification

We'll previously notify the modified place of manufacture, manufactured articles and materials.



SPEC No. :

T.C. : R
Rated voltage : DC 1kV

# Vertical kink long lead (lead style: G / bulk)

Table-3

Your part No.	TDK part No	Cap.	C-Tol.	Dimension		(Unit : mı	m)
Tour part No.	TER Part No	(pF)	(%)	D Max.	T Max.	F	φd
	CK45-R3AD101KAGRA	100	± 10	6.0	5.0	5.0±1.5	0.6
	CK45-R3AD151KAGRA	150	± 10	6.0	5.0	5.0±1.5	0.6
	CK45-R3AD221KAGRA	220	± 10	7.0	5.0	5.0±1.5	0.6
	CK45-R3AD331KAGRA	330	± 10	7.5	5.0	5.0±1.5	0.6
	CK45-R3AD471KAGRA	470	± 10	8.5	5.0	5.0±1.5	0.6
	CK45-R3AD681KAGRA	680	± 10	9.5	5.0	5.0±1.5	0.6
	CK45-R3AD102KAGRA	1000	± 10	11.0	5.0	5.0±1.5	0.6
	CK45-R3AD152KAGRA	1500	± 10	12.0	5.0	7.5±1.5	0.6
	CK45-R3AD222KAGRA	2200	± 10	15.0	5.0	7.5±1.5	0.6

# Vertical kink short lead (lead style: N / bulk)

Table-3 Continue

Your part No.	TDK part No.	Сар.	C-Tol.	Dimension		(Unit : mm)	
Tour part No.	CK45-R3AD101KANRA CK45-R3AD151KANRA CK45-R3AD221KANRA CK45-R3AD331KANRA CK45-R3AD471KANRA	(pF)	(%)	D Max.	T Max.	F	φd
	CK45-R3AD101KANRA	100	± 10	6.0	5.0	5.0±1.5	0.6
	CK45-R3AD151KANRA	150	± 10	6.0	5.0	5.0±1.5	0.6
	CK45-R3AD221KANRA	220	± 10	7.0	5.0	5.0±1.5	0.6
	CK45-R3AD331KANRA	330	± 10	7.5	5.0	5.0±1.5	0.6
	CK45-R3AD471KANRA	470	± 10	8.5	5.0	5.0±1.5	0.6
	CK45-R3AD681KANRA	680	± 10	9.5	5.0	5.0±1.5	0.6
	CK45-R3AD102KANRA	1000	± 10	11.0	5.0	5.0±1.5	0.6
	CK45-R3AD152KANRA	1500	± 10	12.0	5.0	7.5±1.5	0.6
	CK45-R3AD222KANRA	2200	± 10	15.0	5.0	7.5±1.5	0.6

# Vertical kink long lead (lead style: V / Taping)

Table-3 Continue

		าสมเษ-ว	Contint	10				
Your part No.	TDK part No.	Сар.	C-Tol.	Dime	ension	(Unit : n	nm)	Shape
Tour part No.	TBIX part 140.	(pF)	(%)	D Max.	T Max.	F	φd	Chape
	CK45-R3AD101KAVRA	100	± 10	6.0	5.0	5+0.8,-0.2	0.6	Α
	CK45-R3AD151KAVRA	150	± 10	6.0	5.0	5+0.8,-0.2	0.6	Α
	CK45-R3AD221KAVRA	220	± 10	7.0	5.0	5+0.8,-0.2	0.6	Α
	CK45-R3AD331KAVRA	330	± 10	7.5	5.0	5+0.8,-0.2	0.6	Α
	CK45-R3AD471KAVRA	470	± 10	8.5	5.0	5+0.8,-0.2	0.6	Α
	CK45-R3AD681KAVRA	680	± 10	9.5	5.0	5+0.8,-0.2	0.6	Α
	CK45-R3AD102KAVRA	1000	± 10	12.0	5.0	5+0.8,-0.2	0.6	Α
	CK45-R3AD152KAVRA	1500	± 10	14.5	5.0	7.5±0.8	0.6	В
	CK45-R3AD222KAVRA	2200	± 10	15.0	5.0	7.5±0.8	0.6	С



T.C. : R
Rated voltage : DC 2kV

Vertical kink long lead (lead style: G / bulk)

#### Table-4

Your part No.	TDK port No	Сар.	C-Tol.	Dim	ension	(Unit : mm)	
four part No.	TDK part No.	(pF)	(%)	D Max.	T Max.	F	φd
	CK45-R3DD101KAGRA	100	± 10	6.0	6.0	7.5±1.5	0.6
	CK45-R3DD151KAGRA	150	± 10	7.0	6.0	7.5±1.5	0.6
	CK45-R3DD221KAGRA	220	± 10	7.5	6.0	7.5±1.5	0.6
	CK45-R3DD331KAGRA	330	± 10	8.5	6.0	7.5±1.5	0.6
	CK45-R3DD471KAGRA	470	± 10	9.5	6.0	7.5±1.5	0.6
	CK45-R3DD681KAGRA	680	± 10	10.5	6.0	7.5±1.5	0.6
	CK45-R3DD102KAGRA	1000	± 10	12.0	6.0	7.5±1.5	0.6

# Vertical kink short lead (lead style: N / bulk)

Table-4 Continue

Your part No.	TDK part No.	Сар	C-Tol.	Dim	ension	(Unit : mm)	
Tour part No.	TDR part No.	(pF)	(%)	D Max.	T Max.	F	φd
	CK45-R3DD101KANRA	100	± 10	6.0	6.0	7.5±1.5	0.6
	CK45-R3DD151KANRA	150	± 10	7.0	6.0	7.5±1.5	0.6
	CK45-R3DD221KANRA	220	± 10	7.5	6.0	7.5±1.5	0.6
	CK45-R3DD331KANRA	330	± 10	8.5	6.0	7.5±1.5	0.6
	CK45-R3DD471KANRA	470	± 10	9.5	6.0	7.5±1.5	0.6
	CK45-R3DD681KANRA	680	± 10	10.5	6.0	7.5±1.5	0.6
	CK45-R3DD102KANRA	1000	± 10	12.0	6.0	7.5±1.5	0.6

# Vertical kink long lead (lead style: V / Taping)

Table-4 Continue

Vour part No	TDK nort No	Cap. (pF)	C-Tol.	Dimension		(Unit : mm)		Ch and
Your part No.	TDK part No.		(%)	D Max.	T Max.	F	φd	Shape
	CK45-R3DD101KAVRA	100	± 10	6.0	6.0	7.5±0.8	0.6	В
	CK45-R3DD151KAVRA	150	± 10	7.0	6.0	7.5±0.8	0.6	В
	CK45-R3DD221KAVRA	220	± 10	7.5	6.0	7.5±0.8	0.6	В
	CK45-R3DD331KAVRA	330	± 10	8.5	6.0	7.5±0.8	0.6	В
	CK45-R3DD471KAVRA	470	± 10	9.5	6.0	7.5±0.8	0.6	В
	CK45-R3DD681KAVRA	680	± 10	10.5	6.0	7.5±0.8	0.6	В
	CK45-R3DD102KAVRA	1000	± 10	12.0	6.0	7.5±0.8	0.6	В

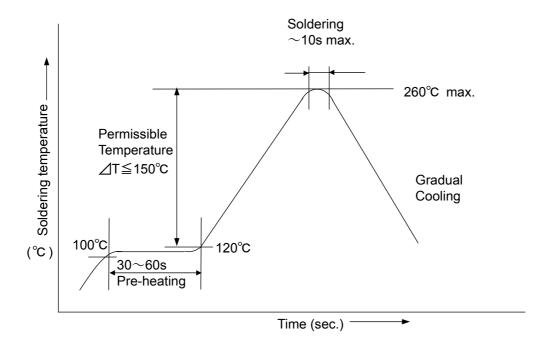
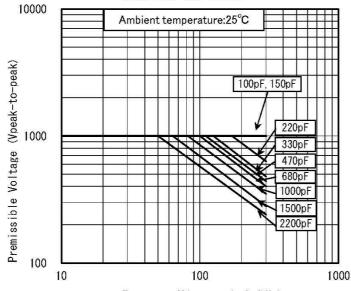
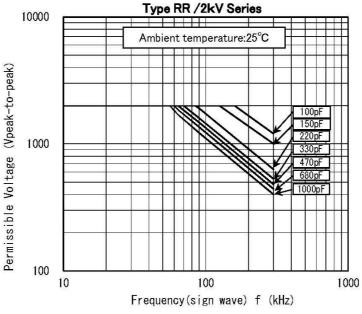


Fig-1

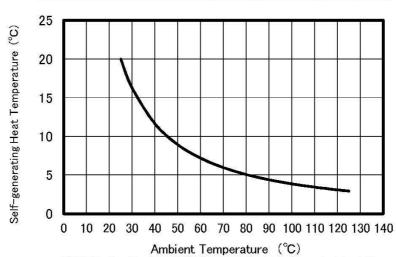
#### Type RR /1kV Series



Frequency (Sign wave) f (kHz)
GRAPH-1.1: Permissible Voltage vs. Applied Voltage Frequency



GRAPH-1.2: Permissible Voltage vs. Applied Voltage Frequency



GRAPH-2: Maximum Self-generating Heat vs. Ambient Temperature

