

# MC3303/MC3403

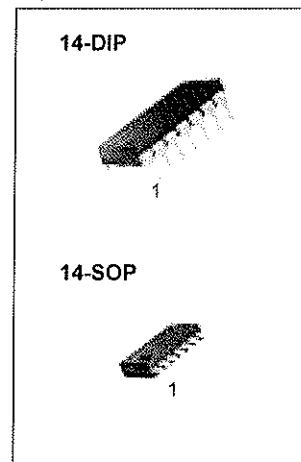
## Quad Operational Amplifier

### Features

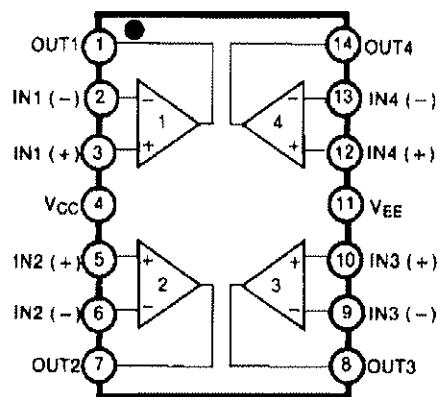
- Output voltage can swing to GND or negative supply
- Wide power supply range;
- Single supply of 3.0V to 36V
- Dual supply of  $\pm 1.5V$  to  $\pm 18V$
- Electrical characteristics similar to the KA741
- Class AB output stage for minimal crossover distortion
- Short circuit protected output.

### Description

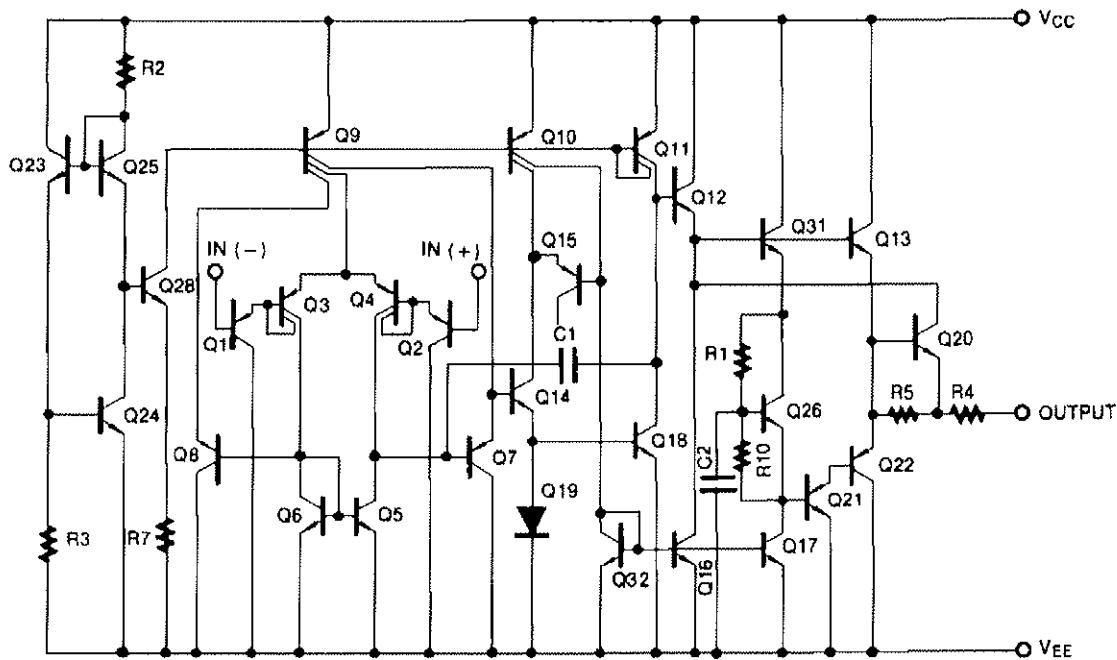
The MC3303/MC3403 series is a monolithic Quad operational amplifier consisting of four independent amplifiers. The device has high gain, internally frequency, compensated operational amplifiers designed to operate from a single power supply or dual power supplies over a wide range of voltages. The common mode input range includes the negative supply, thereby eliminating the necessity for external biasing components in many applications.



### Internal Block Diagram



## Schematic Diagram



## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	V <sub>CC</sub>	$\pm 18$ or +36	V
Differential Input Voltage	V <sub>I</sub> (DIFF)	36	V
Input Voltage	V <sub>I</sub>	$\pm 18$	V
Output Short Circuit Duration	-	Continuous	-
Power Dissipation	P <sub>D</sub>	670	mW
Operating Temperature MC3303 MC3403	T <sub>OPR</sub>	-40 ~ +85 0 ~ +70	°C
Storage Temperature	T <sub>STG</sub>	-65 ~ +150	°C

## Electrical Characteristics

( $V_{CC} = +15V$ ,  $V_{EE} = -15V$  for MC3403,  $V_{CC} = +14V$ ,  $V_{EE} = GND$  for MC3303,  $TA = 25^{\circ}C$ , unless otherwise specified)

Parameter	Symbol	Conditions	MC3303			MC3403			Unit
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Input Offset Voltage	$V_{IO}$	NOTE 1	-	1.5	8.0	-	1.5	10	mV
			-	-	10	-	-	12	
Input Offset Current	$I_{IO}$	NOTE 1	-	5	75	-	5	50	nA
			-	-	150	-	-	100	
Input Bias Current	$I_{BIAS}$	NOTE 1	-	30	200	-	30	200	nA
			-	-	500	-	-	400	
Large Signal Voltage Gain	$G_V$	$V_{O(P-P)} = \pm 10V$	20	200	-	20	200	-	V/mV
		$R_L = 2K\Omega$	15	-	-	15	-	-	
Output Voltage Swing	$V_{O(P-P)}$	$R_L = 10K\Omega$	+12	+12.5	-	$\pm 12$	$\pm 13.5$	-	V
		$R_L = 2K\Omega$	+10	+12	-	$\pm 10$	$\pm 13$	-	
		$R_L = 2K\Omega$ NOTE 1	+10	-	-	$\pm 10$	-	-	
Input Common Mode Voltage Range	$V_{I(R)}$	-	12V - $V_{EE}$	12.5V - $V_{EE}$	-	13V - $V_{EE}$	13.5V - $V_{EE}$	-	V
Common Mode Rejection Ratio	$CMRR$	$R_S \geq 10K\Omega$	70	90	-	70	90	-	dB
Power Supply Current	$I_{CC}$	$V_{O(P)} = 0$ , $R_L = \infty$	-	2.8	7.0	-	2.3	7.0	mA
Output Short Circuit Current	$I_{SC}$	Each amplifier	$\pm 10$	$\pm 30$	$\pm 45$	$\pm 10$	$\pm 20$	$\pm 45$	mA
Positive Supply Rejection Ratio	$PSRR(+)$	-	-	30	150	-	30	150	$\mu V/V$
Negative Supply Rejection Ratio	$PSRR(-)$	-	-	-	-	-	30	150	$\mu V/V$

## Electrical Characteristics (Continued)

(V<sub>CC</sub> = +15V, V<sub>EE</sub> = -15V for MC3403, V<sub>CC</sub> = +14V, V<sub>EE</sub> = GND for MC3303, T<sub>A</sub> = 25 °C, unless otherwise specified)

Parameter	Symbol	Conditions	MC3303			MC3403			Unit
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Average Temperature Coefficient of Input Offset Current (Note2)	$\Delta I_{IO}/\Delta T$	-	-	50	-	-	50	-	pA/ °C
Input Offset Voltage Drift (Note2)	$\Delta V_{IO}/\Delta T$	-	-	10	-	-	10	-	µV/ °C
Power Bandwidth (Note2)	GBW	G <sub>V</sub> =1, R <sub>L</sub> =2KΩ, V <sub>O(P-P)</sub> =20VP-P, THD=5%	-	9.0	-	-	9.0	-	KHz
Small Signal Bandwidth (Note2)	BW	G <sub>V</sub> =1, R <sub>L</sub> =10KΩ, V <sub>O(P-P)</sub> =50mV	-	1.0	-	-	1.0	-	MHz
Slew Rate (Note2)	SR	G <sub>V</sub> =1, V <sub>I</sub> = -10V to +10V	-	0.4	-	-	0.4	-	V/µs
Rise Time (Note2)	TR	G <sub>V</sub> =1, R <sub>L</sub> =10KΩ, V <sub>O(P-P)</sub> =50mV	-	0.35	-	-	0.35	-	µs
Fall Time (Note2)	TF	G <sub>V</sub> =1, R <sub>L</sub> =10KΩ, V <sub>O(P-P)</sub> =50mV	-	0.35	-	-	0.35	-	µs
Over Shoot (Note2)	OS	G <sub>V</sub> =1, R <sub>L</sub> =10KΩ, V <sub>O(P-P)</sub> =50mV	-	20	-	-	20	-	%
Phase Margin (Note2)	MPH	G <sub>V</sub> =1, R <sub>L</sub> =2KΩ, C <sub>L</sub> =200pF	-	60	-	-	60	-	Degree
Crossover Distortion (Note2)	CD	V <sub>I</sub> =30mVP-P, V <sub>O(P-P)</sub> =2.0VP-P, f=10KHz	-	1.0	-	-	1.0	-	%

**Note:**

1. MC3403: 0 °C ≤ T<sub>A</sub> ≤ +70 °C , MC3303: -40 °C ≤ T<sub>A</sub> ≤ +85 °C

2. Guaranteed by design.

## Electrical Characteristics

(VCC = 5.0V, VEE = GND, TA=25 °C unless otherwise specified)

Parameter	Symbol	Conditions	MC3303			MC3403			Unit
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Input Offset Voltage	VIO	-	-	-	10	-	2.0	10	mV
Input Offset Current	IIO	-	-	-	75	-	30	50	nA
Input Bias Current	IBIAS	-	-	-	500	-	200	500	nA
Large Signal Open Loop Voltage Gain	GV	RL = 2.0KΩ	10	200	-	10	200	-	V/mV
Power Supply Rejection Ratio	PSRR	-	-	-	150	-	-	150	µV/V
Output Voltage Range	VO(P-P)	RL = 10KΩ, VCC = 5.0V	3.3	3.5	-	3.3	3.5	-	V
		RL = 10KΩ, 5.0V ≤ VCC ≤ 30V	VCC -2.0	VCC- 1.7	-	VCC -2.0	VCC -1.7	-	
Supply Current	ICC	-	-	2.5	7.0	-	2.5	7.0	mA
Channel Separation	CS	f = 1KHz to 20KHz	-	120	-	-	120	-	dB

## Typical Performance Characteristics

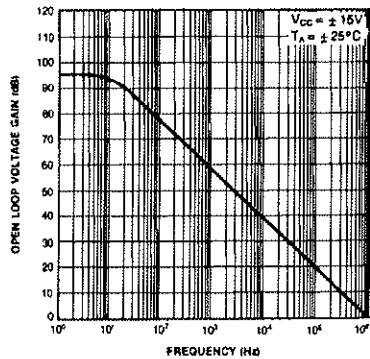


Figure 1. Open Loop Frequency Response

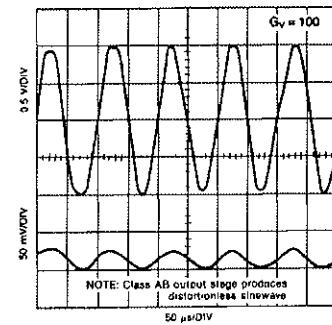


Figure 2. Wave Response

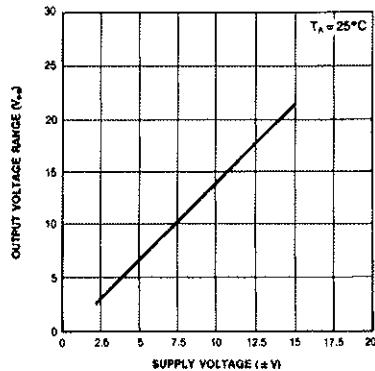


Figure 3. Output Swing

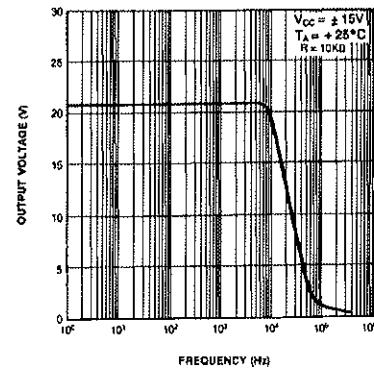


Figure 4. Output Voltage vs Frequency

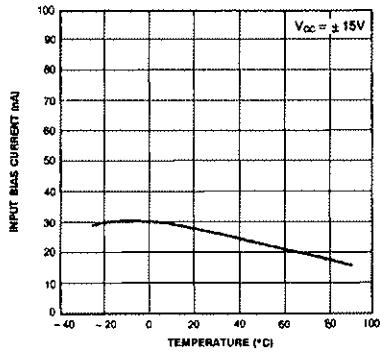


Figure 5. Input Bias Current vs Temperature

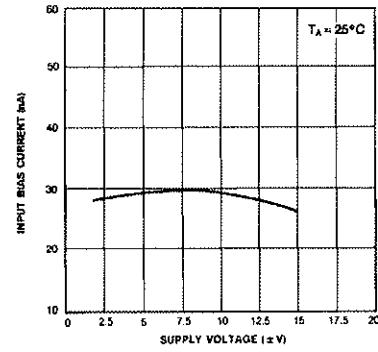


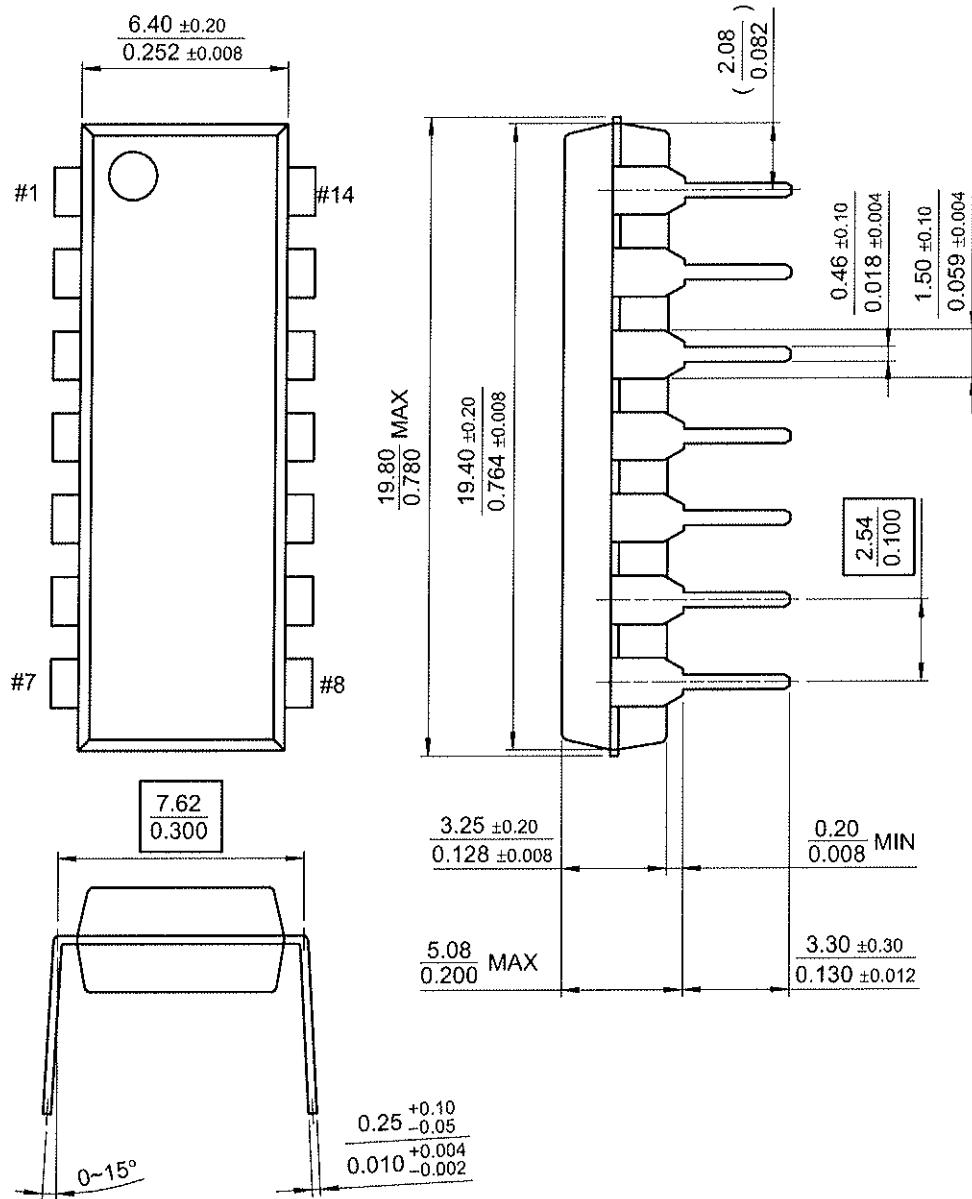
Figure 6. Input Bias Current vs Supply Voltage

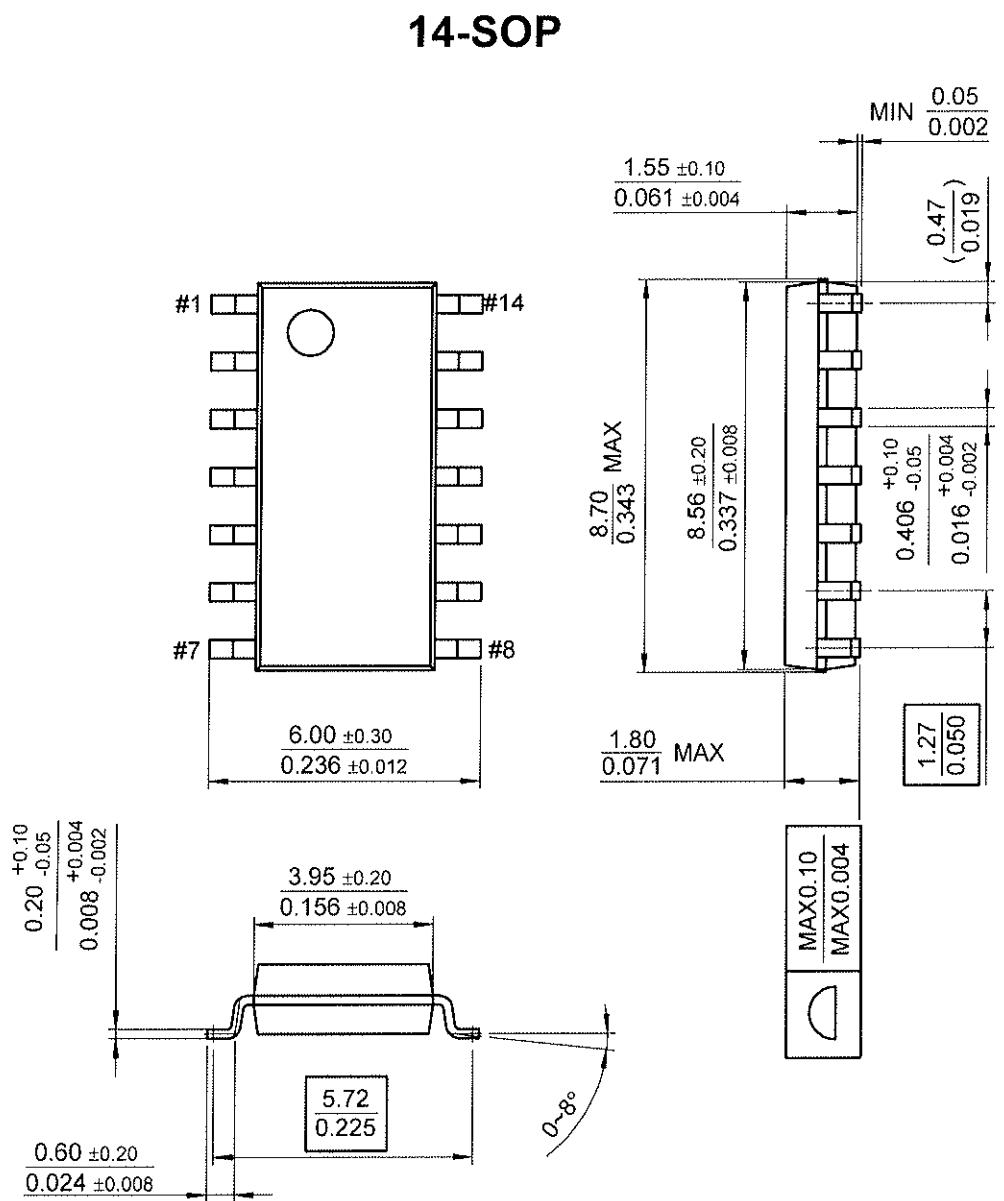
## Mechanical Dimensions

### Package

Dimensions in millimeters

**14-DIP**



**Mechanical Dimensions** (Continued)**Package****Dimensions in millimeters**

## Ordering Information

Product Number	Package	Operating Temperature
MC3403P	14-DIP	0 ~ + 70°C
MC3403D	14-SOP	
MC3303P	14-DIP	-40 ~ + 85°C
MC3303D	14-SOP	

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