

SD560C STANDARD RECTIFIER

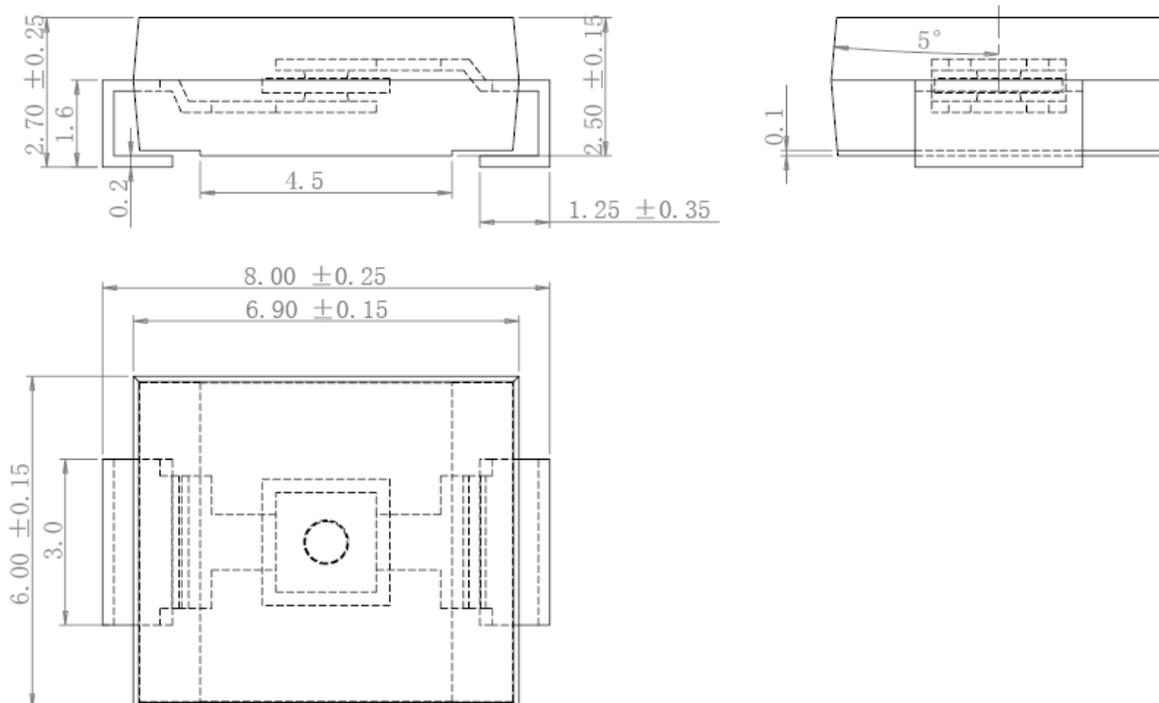
Features:

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop
- Surge Overload Rating to 200A Peak
- Low Power Loss
- Built Strain Relief
- Plastic Case Material has UL Flammability Classification Rating 94V-0
- This is a Pb - Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request

Mechanical data:

- Case: Molded Plastic
- Terminals: Solder Plated , Solderable Per MIL-STD 750 ,Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.65 grams(Approx)

Mechanical Dimensions: In mm



SMC



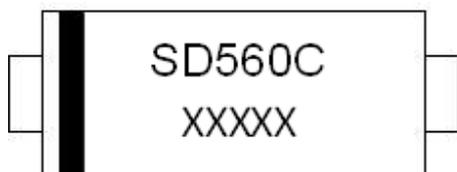
SD560C

Technical Data
Data Sheet N1346, Rev. -

Green Products

Marking Diagram:

Where XXXXX is YYWWL



SD560C = Part Name
YY = Year
WW = Week
L = Lot Number

Cautions: Molding resin
Epoxy resin UL:94V-0

Ordering Information

| Device | Package | Shipping |
|--------|------------------|----------------|
| SD560C | SMC (Pb-Free) | 3000pcs / reel |

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification.

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- China - Germany - Korea - Singapore - United States •
 - <http://www.smc-diodes.com> - sales@smc-diodes.com •



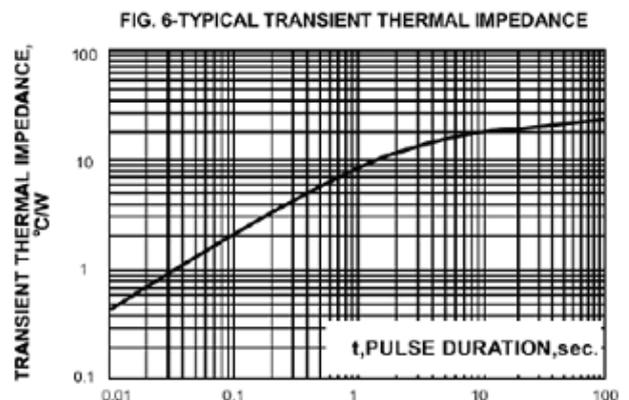
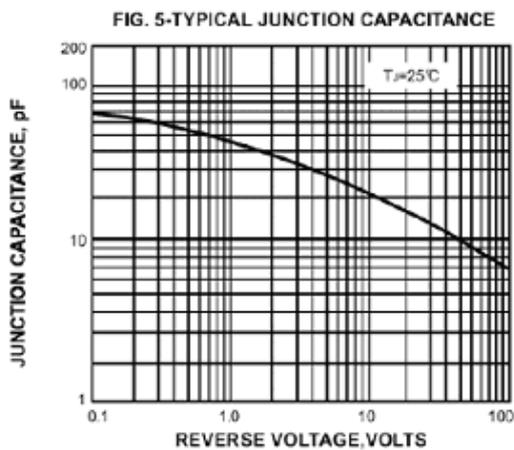
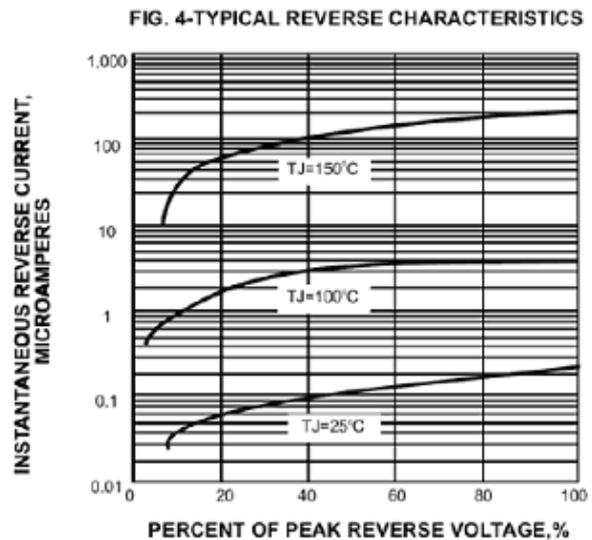
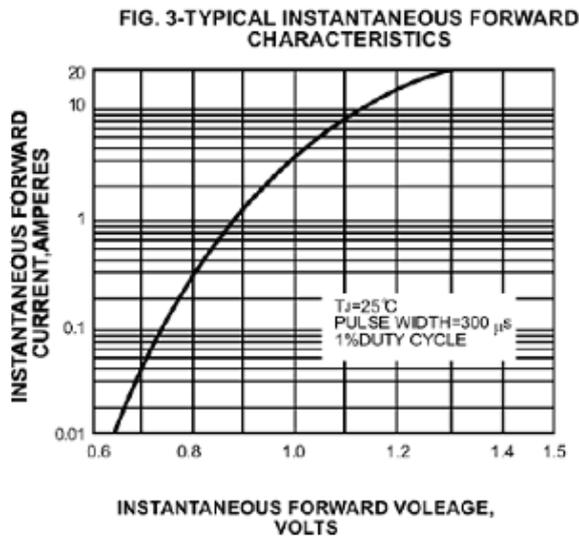
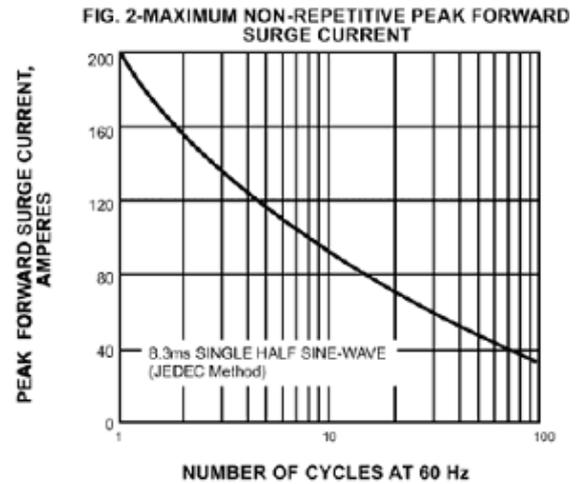
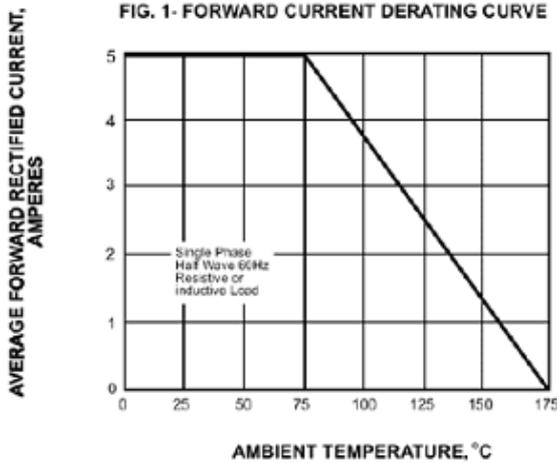
Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
 For capacitive load, derate current by 20%.

| Characteristic | Symbol | SD560C | Unit |
|---|--------------------|-------------|------|
| Maximum Peak Repetitive Reverse Voltage Maximum DC Blocking Voltage | V_{RRM} V_R | 600 | V |
| Maximum RMS Voltage | V_{RMS} | 420 | |
| Maximum Average Forward Rectified Current 0.375"(9.5mm) Lead Length @T _A = 75°C | $I_{(AV)}$ | 5.0 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | I_{FSM} | 200 | A |
| Maximum Instantaneous Forward Voltage @I _F = 5.0A | V_F | 1.2 | V |
| Maximum DC Reverse Current At Rated DC Blocking Voltage @T _A = 25°C 100°C | I_R | 9.0 170 | uA |
| Typical Junction Capacitance (Note 1) | C_j | 50 | pF |
| Typical Thermal Resistance (Note 2) | $R_{\theta JA}$ | 20 | °C/W |
| Operating Storage Temperature Range | T_{STG} | -65 to +175 | °C |
| Operating Junction Temperature | T_J | -65 to +175 | °C |
| Case Style | SMC | | |

Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

2. Thermal resistance form junction to ambient at 0.375" (9.5mm) lead length, P.C.B. mounted





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