



Parameter	Ratings	Units
Blocking Voltage	280	$V_P$
Load Current	200	$mA_{rms} / mA_{DC}$
On-Resistance (max)	10	$\Omega$

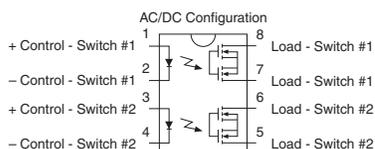
### Features

- 500 $\mu$ s Maximum Switching Times
- High Performance 280V<sub>P</sub> Blocking Voltage
- High Reliability
- 3750V<sub>rms</sub> Input/Output Isolation
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- FCC Compatible
- VDE Compatible
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Small 8-pin Package
- Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Versions Available

### Applications

- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
  - Test Equipment
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

### Pin Configuration



### Description

PAA127 is a dual high performance 280V, 200mA, 10 $\Omega$ , normally open (1-Form-A) Solid State Relay that has two independently controlled, optically coupled MOSFET switches in an 8-pin package. It employs optically coupled MOSFET technology to provide 3750V<sub>rms</sub> of input to output isolation.

Its optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient GaAIAs infrared LED.

This dual single-pole OptoMOS relay provides a more compact design solution than discrete single-pole relays in a variety of applications, and saves board space by incorporating both switches in a single 8-Pin package.

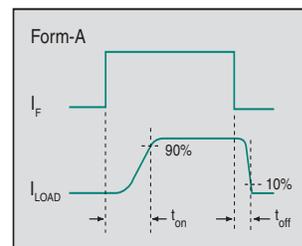
### Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1175739
- Certified to:
  - IEC 60950-1: 2005
  - EN 60950-1: 2006
  - TUV Certificate # B 12 11 82667 002

### Ordering Information

Part #	Description
PAA127	8-Pin DIP (50/Tube)
PAA127S	8-Pin Surface Mount (50/Tube)
PAA127STR	8-Pin Surface Mount (1,000/Reel)
PAA127P	8-Pin Flatpack (50/Tube)
PAA127PTR	8-Pin Flatpack (1,000/Reel)

### Switching Characteristics of Normally Open Devices



### Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	280	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation <sup>1</sup>	150	mW
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 1.33 mW / °C  
<sup>2</sup> Derate linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

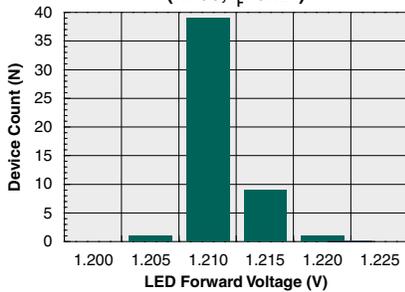
### Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Load Current						
Continuous <sup>1</sup>	-	I <sub>L</sub>	-	-	200	mA <sub>rms</sub> / mA <sub>DC</sub>
Peak	t = 10ms	I <sub>LPK</sub>	-	-	±400	mA <sub>P</sub>
On-Resistance <sup>2</sup>	I <sub>F</sub> =3mA, I <sub>L</sub> =200mA	R <sub>ON</sub>	-	7.3	10	Ω
Off-State Leakage Current	I <sub>F</sub> =0mA, V <sub>L</sub> =280V <sub>P</sub>	I <sub>LEAK</sub>	-	1	25	nA
Switching Speeds						
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	0.42	0.5	ms
Turn-Off		t <sub>off</sub>	-	0.12	0.5	
Output Capacitance	I <sub>F</sub> =0mA, V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	16	-	pF
<b>Input Characteristics</b>						
Input Control Current to Activate <sup>3</sup>	I <sub>L</sub> =200mA	I <sub>F</sub>	-	0.5	3	mA
Input Control Current to Deactivate	-	-	0.3	0.49	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Common Characteristics</b>						
Input to Output Capacitance	-	C <sub>IO</sub>	-	3	-	pF

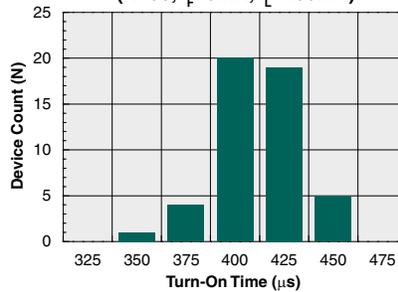
<sup>1</sup> If both poles operate, the load current must be derated in order not to exceed the package power dissipation value.  
<sup>2</sup> Measurement taken within one (1) second of on-time.  
<sup>3</sup> For high-temperature applications (>60°C) use a LED current of at least 5mA.

PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*

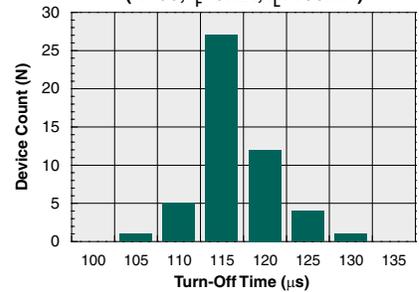
Typical LED Forward Voltage Drop  
(N=50, I<sub>F</sub>=5mA)



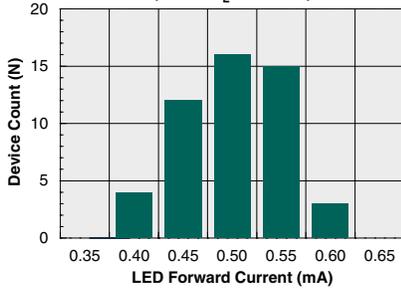
Typical Turn-On Time  
(N=50, I<sub>F</sub>=5mA, I<sub>L</sub>=100mA)



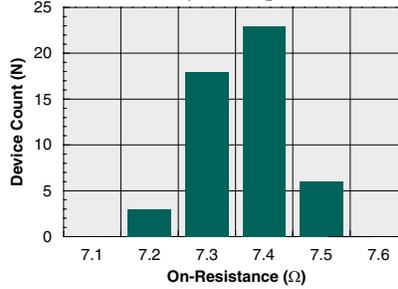
Typical Turn-Off Time  
(N=50, I<sub>F</sub>=5mA, I<sub>L</sub>=100mA)



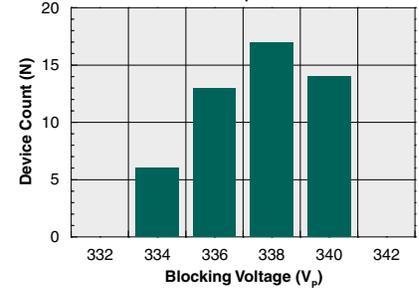
Typical I<sub>F</sub> for Switch Operation  
(N=50, I<sub>L</sub>=200mA)



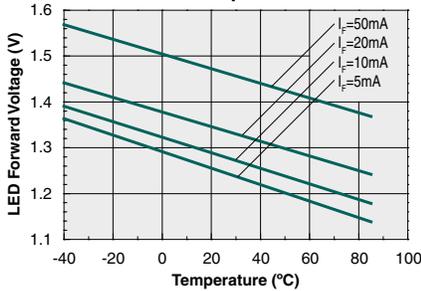
Typical On-Resistance Distribution  
(N=50, I<sub>F</sub>=3mA, I<sub>L</sub>=200mA)



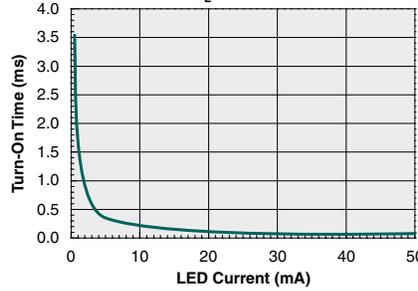
Typical Blocking Voltage Distribution  
(N=50, I<sub>F</sub>=0mA)



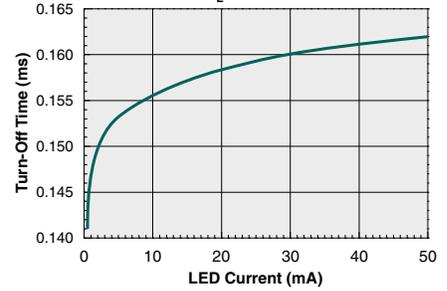
Typical LED Forward Voltage Drop  
vs. Temperature



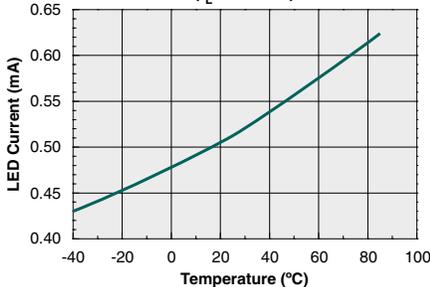
Typical Turn-On Time  
vs. LED Forward Current  
(I<sub>L</sub>=100mA)



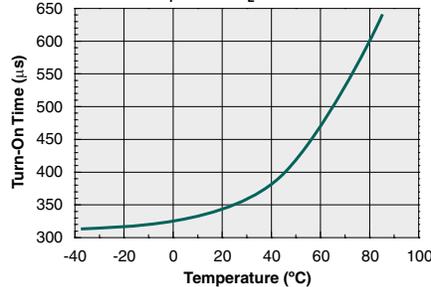
Typical Turn-Off Time  
vs. LED Forward Current  
(I<sub>L</sub>=100mA)



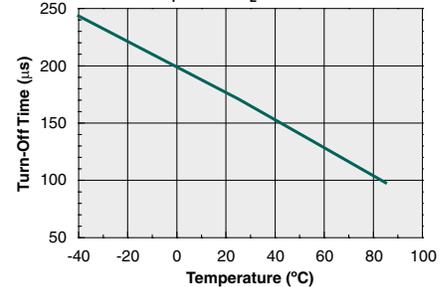
Typical I<sub>F</sub> for Switch Operation  
vs. Temperature  
(I<sub>L</sub>=200mA)



Typical Turn-On Time vs. Temperature  
(I<sub>F</sub>=5mA, I<sub>L</sub>=100mA)



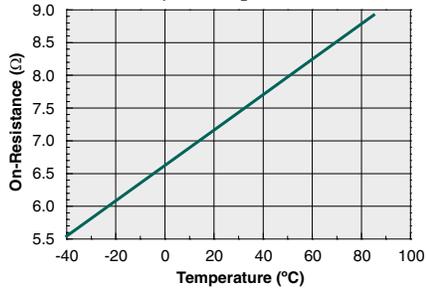
Typical Turn-Off Time vs. Temperature  
(I<sub>F</sub>=5mA, I<sub>L</sub>=100mA)



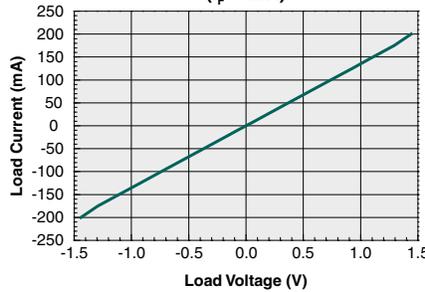
\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

**PERFORMANCE DATA @25°C (Unless Otherwise Noted)\***

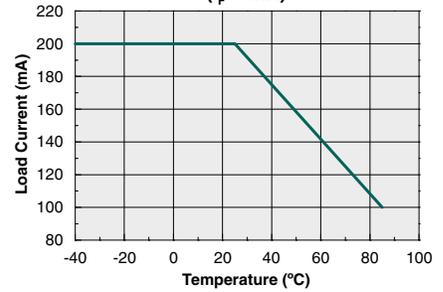
**Typical On-Resistance vs. Temperature**  
( $I_F=3mA, I_L=200mA$ )



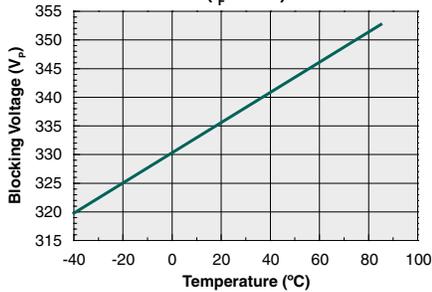
**Typical Load Current vs. Load Voltage**  
( $I_F=3mA$ )



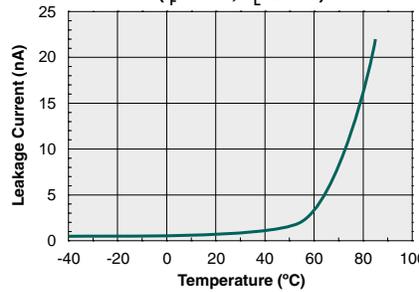
**Maximum Load Current vs. Temperature**  
**AC/DC Configuration**  
( $I_F=3mA$ )



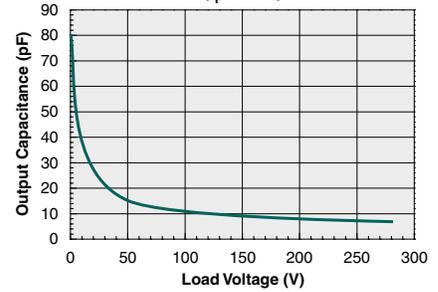
**Typical Blocking Voltage vs. Temperature**  
( $I_F=0mA$ )



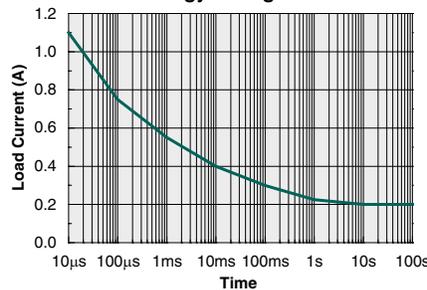
**Leakage Current vs. Temperature**  
**Measured Across Pins 5&6 & 7&8**  
( $I_F=0mA, V_L=280V$ )



**Output Capacitance vs. Load Voltage**  
( $I_F=0mA$ )



**Energy Rating Curve**



\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Manufacturing Information

### Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
PAA127 / PAA127S / PAA127P	MSL 1

### ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

### Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
PAA127 / PAA127S	250°C for 30 seconds
PAA127P	260°C for 30 seconds

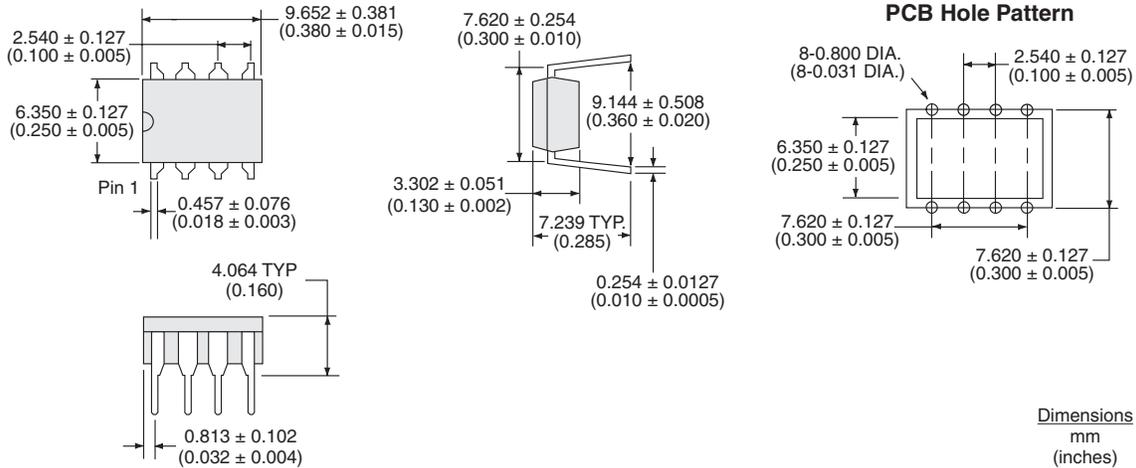
### Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

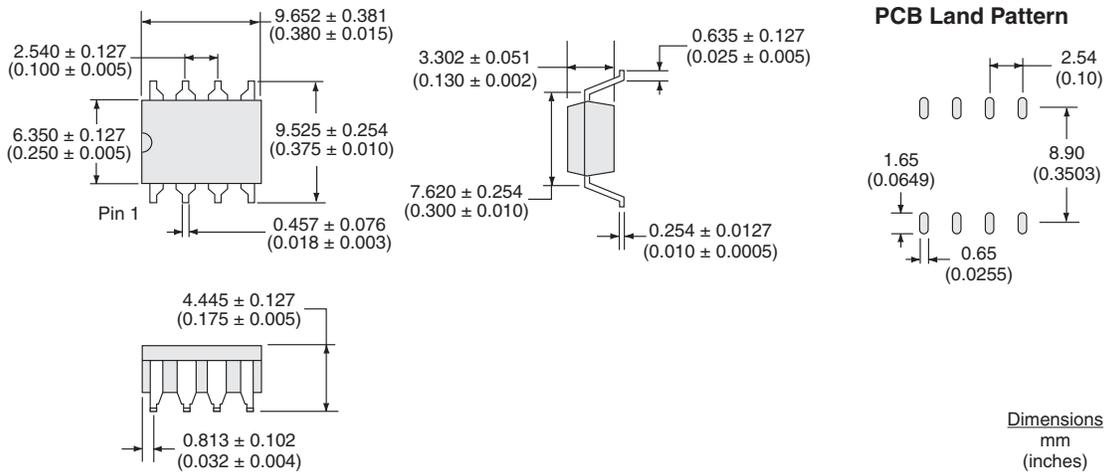


**MECHANICAL DIMENSIONS**

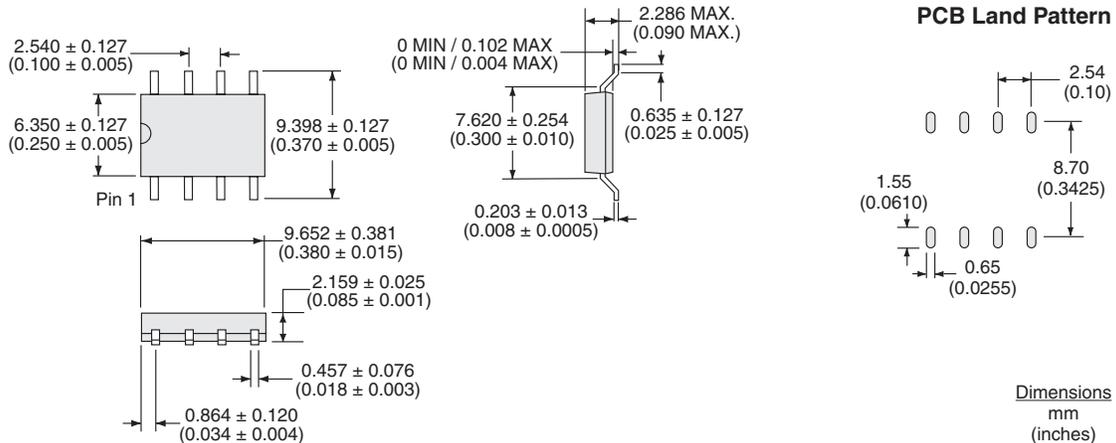
**PAA127**



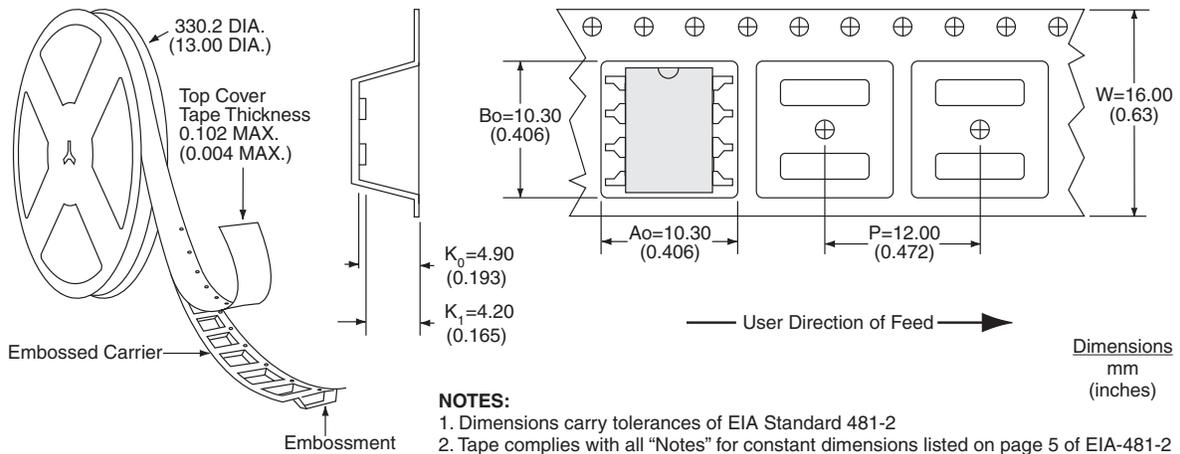
**PAA127S**



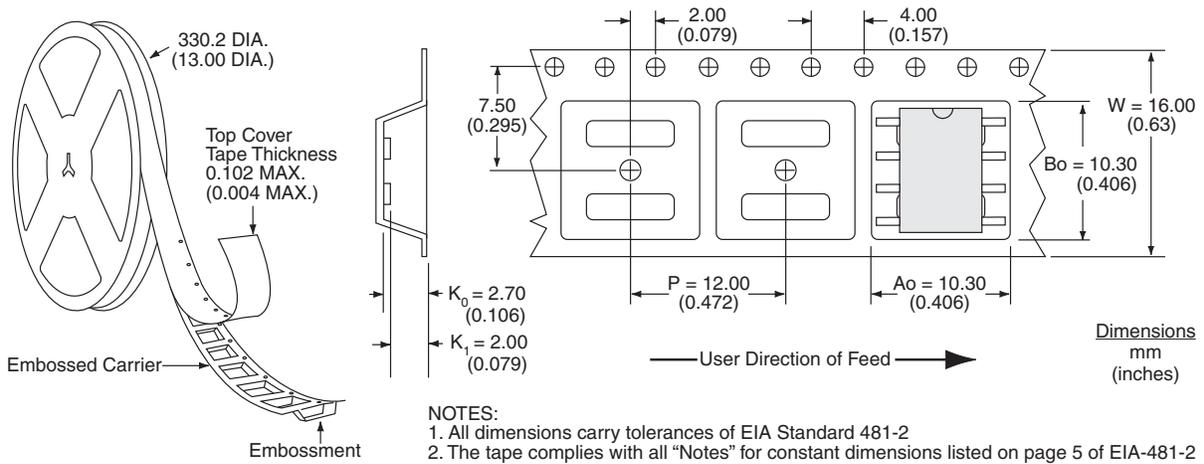
**PAA127P**



### PAA127STR Tape & Reel



### PAA127PTR Tape & Reel



For additional information please visit our website at: [www.ixysic.com](http://www.ixysic.com)

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